PW-19

The study of the interface between GaAs substrate and the regrowth GaAs layer formed by MBE and MOCVD

Fujimoto, H., Tanabe, M., Tamura, A.

Electronics Research Lab., Matsushita Electronics Corporation, 3-1-1 Yagumo-nakamachi, Moriguchi, Osaka 570, Japan

We study the interface between GaAs substrate and the regrowth GaAs layer formed by MBE and MOCVD. It was found that hole trap levels exist in the MBE regrowth interface, but no trap levels exist in the MOCVD regrowth interface by using DLTS analysis. From SIMS analysis, the carbon impurities of the MOCVD regrowth interface is much lower than that of MBE. MOCVD regrowth can achieve the good regrowth interface characteristics.

PW-20

Mobility Model for III-V Compounds Suited for Hydrodynamic Device Simulation

Kopf, Ch., Kosina, H., Selberherr, S.

Inst. for Microelectronics, TU Vienna, GuBhausstraBe 27-29, A-1040 Vienna, Austria

An improved model for the electron mobility as function of the total electron energy is proposed which is needed in hydrodynamic device simulation. It consists of fit formulas for the individual valley mobilities which are combined by means of the relative valley population Maxwell-Boltzmann distribution. It reflects the situation of intervalley transfer in compounds much better than the models now mostly used and is based on steady-state Monte Carlo calculations. The method can be well applied to III-V alloys, the coefficients being low order polynomials of the material composition.