

Systematical Study of InAlN/GaN Devices by Numerical Simulation

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Abstract

InAlN/GaN HEMTs have been proposed to provide higher polarization charges without the drawback of high strain [1]. Several groups have demonstrated devices based on InAlN/GaN ([2], [3]), with maximum current capabilities surpassing those of AlGaIn/GaN structures. For further optimization a reliable simulation tool is needed. Our two-dimensional device simulator MINIMOS-NT is expanded by material models for InAlN. Relying on experimental work, we conduct a simulation study of InAlN HEMTs. Using a calibrated setup, we explore device specific effects and we estimate the AC device performance.

1. DEVICE STRUCTURE

Optimization of the structures has been carried out by using analytical models [1]. In order to fully develop the potential of the device, proper modeling of the materials is required. However, many of the material properties (e.g. bandgap bowing parameter varying from 2.5 eV up to 4.0 eV) are still subject of discussion. Based on our previous work on InN and AlN we introduce a material model for InAlN, which is then incorporated in the two-dimensional device simulator MINIMOS-NT, thus enabling the simulation and optimization of novel InAlN/GaN structures. We use HEMT structures described in [3] and [4] to benchmark the DC and AC simulation results against measured data. Schematic layer structure of the investigated $\text{In}_{0.2}\text{Al}_{0.8}\text{N}/\text{GaN}$ device [4] is shown in Fig. 1. All layers are non-intentionally doped.

2. DEVICE MODELING AND SIMULATION RESULTS

Since the longitudinal electric fields in the channel reach high peak values [5], we employ a hydrodynamic transport model. We assume a bandgap bowing factor of 3 eV for InAlN. This yields a bandgap of 4.58 eV for $\text{In}_{0.2}\text{Al}_{0.8}\text{N}$ at 300 K (Fig. 2). The values for the band offsets are $\Delta E_C=0.66$ eV and $\Delta E_V=0.59$ eV, corresponding to a 53%/47% setup. The calculated dielectric permittivity of $\text{In}_{0.2}\text{Al}_{0.8}\text{N}$ is 9.86, which is in a good agreement with the value listed in [4]. The barrier height of the gate Schottky contact is 1 eV. The value of the sheet charge at the InAlN/GaN interface induced by the polarization effects is found to be $3.3 \times 10^{13} \text{ cm}^{-2}$ from the DC characteristics (simulation results for different values are given in Fig. 3). A commensurate negative surface charge (as the device is not passivated, a low value of $1.0 \times 10^{13} \text{ cm}^{-2}$ is assumed) at the top of the InAlN surface is also considered in the simulation. Simulation results for the transfer characteristics assessing different charge values are shown in Fig. 4. Self-heating effects are accounted for by using thermal resistance of $R_{th}=3$ K/W at the substrate thermal contact (Fig. 5 compares transfer characteristics without self-heating effects and with different values of R_{th}). This value lumps the thermal resistance of the nucleation layer and the sapphire substrate, and possible three-dimensional thermal effects. Our simulation exhibits good agreement with the experimental data under consideration of ohmic contact resistances $R_C=1.3 \text{ } \Omega\text{mm}$. Simulated output characteristics show a good agreement with the experimental data (Fig. 6). By AC analysis of the device, cut-off frequency $f_T \approx 7$ GHz is obtained. This low value can be explained with the conservative design of the device and the low carrier mobility in the channel ($\mu=230 \text{ cm}^2/\text{Vs}$). Downscaled devices are analyzed ($l_g=0.5/0.25 \text{ } \mu\text{m}$) and the effect of higher quality GaN material on AC performance is studied. In another simulation, a $l_g=0.25 \text{ } \mu\text{m}$ device reported in [3] (carrier mobility $\mu=530 \text{ cm}^2/\text{Vs}$) reaches $f_T=36$ GHz.

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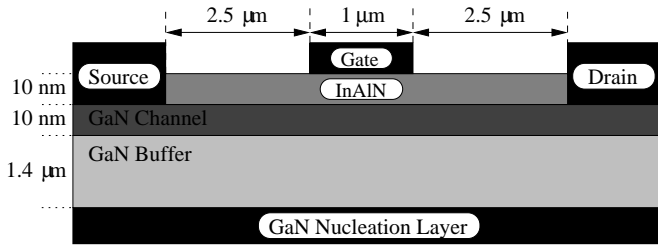


Fig. 1: Schematic layer structure of the investigated device.

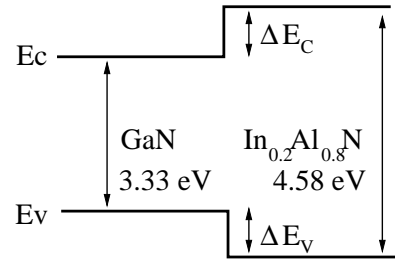


Fig. 2: Band alignment of the heterointerface.

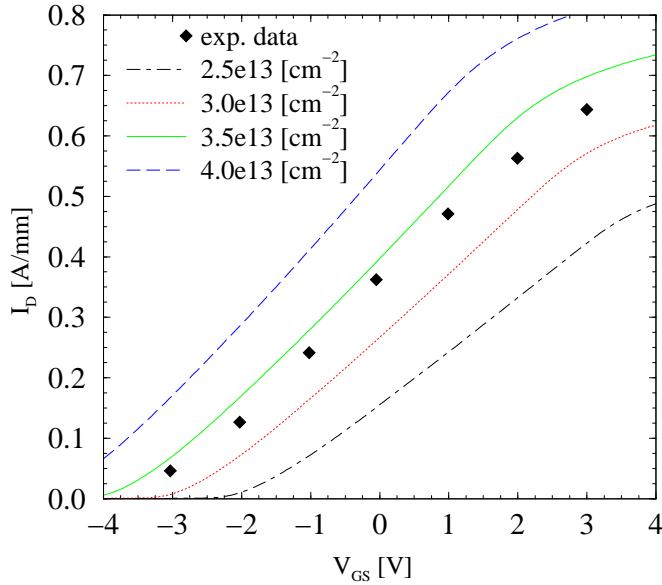


Fig. 3: Transfer characteristics for different values of the polarization charge at the InAlN/GaN interface and $-1.0 \times 10^{13} \text{ cm}^{-2}$ at the top of the InAlN interface ($V_{ds}=8.0 \text{ V}$).

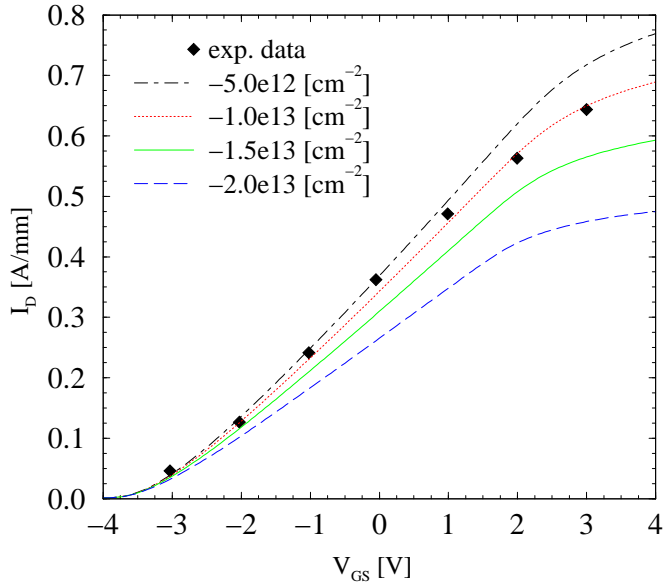


Fig. 4: Transfer characteristics for different values of the total charges (polarization and traps) at the top of the InAlN surface and $3.3 \times 10^{13} \text{ cm}^{-2}$ at the InAlN/GaN interface ($V_{ds}=8.0 \text{ V}$).

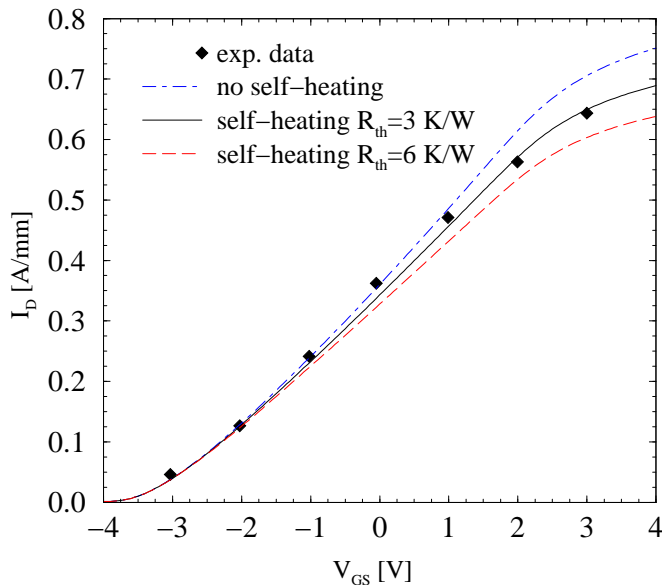


Fig. 5: Comparison of simulated transfer characteristics for different values of the thermal resistance and experimental data ($V_{ds}=8.0 \text{ V}$).

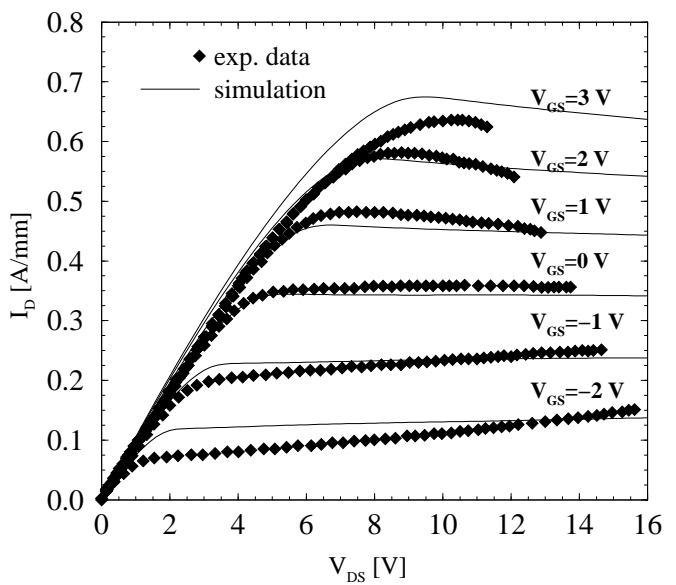


Fig. 6: Comparison of simulated output characteristics and experimental data.