Switching Oxide Traps as the Missing Link Between Negative Bias Temperature Instability and Random Telegraph Noise


Abstract

Due to the ongoing reduction in device geometries, the statistical properties of a few defects can significantly alter and degrade the electrical behavior of nano-scale devices. These statistical alterations have commonly been studied in the form of random telegraph noise (RTN). Here we show that a switching trap model previously suggested for the recoverable component of the negative bias temperature instability (NBTI) can more accurately describe the bias and temperature dependence of RTN than established models. We demonstrate both theoretically and experimentally, that the recovery following bias temperature stress can be considered the non-equilibrium incarnation of RTN, caused by similar defects. We furthermore demonstrate that the recoverable component is solely constituted by individual and uncorrelated discharging of defects and that no diffusive component exists. Finally it is highlighted that the capture and emission times of these defects are uncorrelated.

Introduction

In future nano-scale MOSFETs only a handful of defects will be present in the oxide above the channel region which can have a significant stochastic impact on their operation [1]. In order to understand circuits using such devices, as well as being able to estimate their reliability, one has to study the dynamic behavior of these defects [2]. We have recently reported experimental data which show that both 1/f noise and negative bias temperature instability (NBTI) in pMOSFETs are due to defects with very similar properties [3] and we have already successfully described their voltage and temperature dependence [4]. Consequently, the defects responsible for random telegraph noise, which have been suspected to also be the fundamental building blocks of 1/f noise [5, 6], could play a similar role in NBTI.

It has been demonstrated that the reduction in the random telegraph noise power brought about by bias switching can be described by a charge trapping model because the defects have to adjust to the new bias condition [7]. Here we will show that the same is also valid under the heavy stress conditions typical for NBTI by demonstrating that both the quasi-equilibrium (RTN) and the non-equilibrium (i.e., NBTI stress and recovery) behavior can be successfully described by our switching trap model. The main difference is that defects with larger time constants are activated in NBTI, resulting in the characteristic long relaxation curves with time constants below 1μs and longer than 11 days [8]. The theoretical predictions obtained from our model will be confirmed by carefully designed experiments recorded on nano-scale pMOS-FETs (W/L = 150nm/100nm). A particularly intriguing observation further supporting the idea that individual switching traps constitute the overall degradation is that the capture and emission times are essentially uncorrelated. Based on this evidence it also has to be concluded that the characteristic switching behavior is not due to a diffusive process, such as assumed in the popular reaction-diffusion (RD) theory [9, 10].

Previous Modeling Approaches for RTN

Modeling of RTN and 1/f noise dates back to the work of McWhorter [11]. Irrespective of the fact that variants of this elastic tunneling model are still frequently used, it has been repeatedly shown that they can neither explain the temperature nor the bias dependence of the time constants [6, 12–14]. Kirton and Uren have used a lattice-relaxation multiphonon emission (LRME) process [6], see Fig. 1, but also observed that the required capture cross sections showed a bias dependence stronger than the expected 1/p behavior (p being the surface hole concentration), see Fig. 2. This mismatch has repeatedly been demonstrated and prompted various authors to introduce empirical corrections to the capture cross sections [15].

For thicker oxides such a strong bias dependence has often been successfully explained by the Coulomb blockade [13, 14], which is introduced by having to move the mirror charges against the external bias source. However, this explanation fails for thin oxides, as there for a defect inside the oxide the Coulomb barrier is dominated by the mirror charge on the gate, resulting even in a turn-around of the bias dependence [16], see Fig. 3. We consequently have to conclude that the experimentally observed bias dependence cannot be properly explained by existing theories [15].

The Switching Trap Model for RTN and NBTI

We will thus model RTN using the Harry Diamonds Lab (HDL) switching trap model [17] previously suggested for the description of recoverable charge trapping in NBTI [4], see Fig. 4. It assumes that charge trapping creates a defect, possibly an E’ center, which can be repeatedly charged and discharged and anneals only when in the neutral state. For most purposes, charging of the already created defect can be considered fast, allowing us to derive simpler expressions for the effective capture and emission times (the slow defect creation and annealing) using an ‘effective Poissonian model’ (middle of Fig. 4). In comparison to previously published models, the switching trap model can predict a much stronger bias dependence of the time constants: for the capture time this is a consequence of the pre-cursor defect level lying below
that hole capture and emission are directly linked to the creation and annealing of interface states, controlled by a diffusive process. Although RD theory has as of yet only been studied in its macroscopic form, a nano-scale representation of RD theory can be obtained by expressing the electro-chemical reaction at the interface and the subsequent diffusion using stochastic differential equations. The recovery behavior of the stochastic RD model is shown in Fig. 7, which, as expected, also proceeds in steps. However, when a number of such traces is averaged, the behavior of the macroscopic RD model with a single transition lasting about 4 decades is obtained. Furthermore, since the RD model assumes no dispersion in either the interface reaction nor in the diffusion [19], all devices behave identically. We finally remark that in charge trapping models the steps always occur at the same time, while in a diffusive mechanism the single-big-step has its inflection point when the recovery time equals the stress time [20] \([t_s = t_r]\), see Fig. 8. These theoretical predications can be used to experimentally differentiate between a charge trapping or a diffusive mechanism.

**Experimental Validation**

Experimental evidence was gathered on narrow SiON devices with \(t_{ox} = 2.2\) nm and 1.8 nm, see [21] for details. Ensuring complete recovery, devices were repeatedly stressed under the same conditions and the data were averaged. Depending on the stress condition a permanent degradation was also observed [4, 22], probably due to interface states, which, however, does not contribute to the slow switching behavior.

A typical example showing the contribution of a single defect is given in Fig. 9. Longer stresses activate more defects as shown in Fig. 10 and the capture and emission times are clearly uncorrelated. Fig. 11 demonstrates the temperature dependence of both the capture and the emission time of a single defect, consistent with the switching trap model, while Fig. 12 shows that the averaged traces do not depend on the stress time. These data convincingly confirm that, just like with RTN, the physical mechanism behind the recoverable component of NBTA is random trapping and detrapping of charge as described by the switching trap model because (i) the characteristic emission times of each trap are fixed in time (if we were dealing with a diffusion-controlled mechanism, at least a hint of ‘moving traces’ should be detectable while not a single one was found), (ii) each averaged switching event covers about 1.3 decades in time as predicted by the model (classical diffusion covers about 3.8 decades), (iii) the capture and emission times are uncorrelated (this is definitely not possible with a diffusion controlled mechanism), and (iv) both NBTA and RTN can be explained by a single model.

**Conclusions**

We have demonstrated both theoretically and experimentally that RTN and the recoverable component of NBTA are due to charge trapping in switching oxide traps, the main difference being that NBTA stress activates defects with larger time constants. Most importantly, the capture and emission times of the defects are uncorrelated, revealing for the first time explicitly that individual defects constitute the recovery of NBTA.
The oxide trap level was suggested by Kirton and Uren and is based on a LRME process. The oxide trap level $E_T = E_{\text{CB}} - \phi_f$ is assumed to lie within the silicon bandgap where $\Delta E_T = E_{\text{CB}} - E_{\text{FO}}$ determines the magnitude of the emission time constant $\tau_E$. The oxide trap level is the LRME barrier, $E_f$ the modulus of the oxide field, $\beta = k_BT$, $V_f = k_BT/q$, $x$ the distance of the defect into the oxide, $\sigma_f$ the surface potential, $\sigma_{fo}$ from a simple WKB approximation, and $\sigma$ the capture cross section.

$$
\tau_E = \frac{\hbar^2}{2m^*} \frac{N_f}{p} e^{-k_BT} \left( \frac{x}{e^{F/\Delta E_T}} \right)
$$

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Fig 1: The standard model for RTN and 1/f noise was suggested by Kirton and Uren and is based on a LRME process. The oxide trap level $E_T = E_{\text{CB}} - \phi_f + qV_f$ is assumed to lie within the silicon bandgap where $\Delta E_T = E_{\text{CB}} - E_{\text{FO}}$ determines the magnitude of the emission time constant $\tau_E$. The oxide trap level is the LRME barrier, $E_f$ the modulus of the oxide field, $\beta = k_BT$, $V_f = k_BT/q$, $x$ the distance of the defect into the oxide, $\sigma_f$ the surface potential, $\sigma_{fo}$ from a simple WKB approximation, and $\sigma$ the capture cross section.

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Fig 2: Comparison of experimental data for two selected (because difficult to model) defects in a SiON pMOS with $t_{ox} = 1.8\mu$m. While the Kirton/Uren model qualitatively fits the bias dependence it cannot properly capture the strong bias dependence of both $\tau_E$ and $\tau_r$ at the same time. Fits of similar quality can be found in literature [7, 15] and are not a peculiarity of the selected defect.

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Fig 3: No improvement is obtained in the Kirton/Uren model when the Coulomb blockade (CB) is considered since in a thin oxide the CB energy begins to increase with increasing $[V_f]$ even for a repulsive defect located inside the oxide [16].

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Fig 4: Left: The Harry-Diamond-Labs (HDL) switching trap model [17] used previously to model the recoverable charge trapping component of NBTI (stage one) [4]. It assumes that a defect is created from the precursor state 1 by capturing a hole via a field-assisted LRME mechanism [23, 24]. Once the defect is created, the charge state quickly follows the Fermi-level, being positive in state 2 and neutral in state 3. Full annealing of the defect is possible from the neutral state 3 only. Middle: Under the assumption that the transition rates between states 2 and 3, $k_2$ and $k_3$, are much larger than $k_{12}$ and $k_{32}$, effective rates $k_{23} \approx k_{12}$ and $k_{32} \approx k_{31}/(1 + k_{32}/k_{32})$ can be defined. The factor $1/(1 + k_{32}/k_{32})$ gives the occupancy of state B according to Fermi-Dirac statistics and considerably increases the charge trapping time $\tau_E = 1/k_{23}$ when the defect is positively charged. The parameters are the precursor energy level $E_T$ which is assumed to lie below the valence band edge ($\Delta E_T < 0$), the energy level of the created defect $\Delta E_f = E_{\text{CB}} - E_{\text{FO}}$ assumed to be inside close to the valence band edge ($\Delta E_f > 0$), $F_c$ the reference field for the field-assisted LRME process, $\nu = 10^{13} s^{-1}$ the phonon frequency. Right: The switching trap model accurately predicts the temperature and bias dependence of the capture and emission time-constants. When the precursor level $E_T$ crosses the valence band edge $E_V$, a transition to the standard bias dependence $1/p$ is observed. Such a behavior has been experimentally observed [25].

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Fig 5: Simulated RTN, NBTI stress, and NBTI recovery behavior of a nano-scale device using the stochastic solution algorithm (SSA) [26]. Results are obtained from a stochastic version of the switching trap model of Fig. 4. The model parameters are taken from an extensive calibration to ultrathin SiON devices [4] but the number of defects was reduced to 5, qualitatively demonstrating the response in a nano-scale device. Left: At the threshold voltage ($V_{th}$), the RTN is dominated by defect #5 with the occasional contribution from defect #3. Defects #1, #2, and #4 remain positively charged within the ‘simulation/experimental’ window. Middle: During NBTI stress ($V_{th}$), the capture times are dramatically reduced by the higher (more negative) gate voltage and the defects #3 and #5 become predominantly positively charged ($\tau_c < \tau_E$). Defects #1, #2, and #4 start producing RTN with $\tau_c < \tau_E$. Right: During NBTI recovery (back at $V_{th}$), trapped charge is subsequently lost and the quasi-equilibrium behavior is gradually restored.
Averaged recovery traces due to a single defect at four different temperatures. Only when recovery traces of the same device are averaged, the time-constants can be extracted. Application of the above procedure to different devices reveals that each device has a different individual set of defects with different time-constants.

Experimental recovery traces of a defect with characteristic step-height of 1.5 mV. The effective time-constant of the averaged traces with increasing stress time shows that defects created after longer stress times can emit with increasingly shorter times. Since no dispersion is considered in the RD model, the stochastic RD model also shows random step-though in the recovery traces results in clearly visible steps, cf. Fig. 6. We remark that the 32 consecutive stresses of 1 s introduce an increasing permanent contribution which does not recover within the experiment window [4–22] and is not further studied here. Right: The evolution of the averaged recovery traces with increasing stress time shows that defects created after longer stress times can emit their hole prior to defects created earlier. For example, the first defect to become charged (visible after 100 µs) has an emission time constant of about 50 s (τ1) while only after 5 s of stress the defects with emission time constants 10 ms and 5 s are created. This demonstrates that capture and emission times in nano-scale devices are essentially uncorrelated.

References