

Using Strain to Increase the Reliability of Scaled Spin MOSFETs

D. Osintsev^{1,2}, V. Sverdlov¹, A. Makarov¹, and S. Selberherr¹

¹Institute for Microelectronics, TU Wien, Gußhausstraße 27-29, A-1040 Vienna, Austria

²Volgograd State Technical University, Lenin Avenue 28, 400131 Volgograd, Russia

E-mail: {Osintsev|Sverdlov|Makarov|Selberherr}@iue.tuwien.ac.at

Abstract—We investigate the surface roughness induced spin relaxation in scaled spin MOSFETs. We show that the spin-flip hot spots characterized by strong spin relaxation appear in thin MOSFET channel. Strain can efficiently move these hot spots outside of the states occupied by carriers, resulting in a substantial increase of the spin lifetime.

Keywords—surface-roughness relaxation; phonon relaxation; ultra-scaled SOI MOSFETs; shear strain

I. INTRODUCTION

Novel microelectronic devices have to be smaller and faster than the traditional ones and be more efficient in order to reduce power consumption of future integrated electronic circuits. A promising alternative to the charge degree of freedom currently used in MOSFET switches is to take into account the electron spin. Spintronics attracts much attention, and a number of novel devices has already been proposed [1,2]. Silicon is an ideal material for spintronic devices [3], because it is composed of nuclei with predominantly zero spin and is characterized by small spin-orbit coupling. Both factors favor to reduce spin relaxation. Spin transport through a 350μm thick silicon wafer was demonstrated at low temperature [4]. Spin propagation at such long distances combined with a possibility of injecting spin at room temperature [5] makes the fabrication of spin-based switching devices in the near future plausible. However, the experimentally observed enhancement in spin relaxation in electrically gated lateral-channel silicon structures [6] could compromise the reliability and become an obstacle in realizing spin-driven devices. Thus, deeper understanding of spin relaxation mechanisms in silicon is urgently needed [7].

We investigate the surface roughness induced spin relaxation in an ultra-scaled double-gate spin MOSFETs. We predict a substantial increase of the spin lifetime with uniaxial strain applied.

II. MODEL DESCRIPTION

We consider (001) oriented thin silicon films. In order to

find the surface roughness induced spin relaxation matrix elements the subband wave functions and subband energies have to be known. A perturbative $\mathbf{k} \cdot \mathbf{p}$ approach [8–10] is suitable to describe the electron subband structure in the presence of strain and spin-orbit interaction. We consider only the two relevant valleys along the [001] axis. The Hamiltonian is written in the vicinity of the X -point along the k_z -axis in the Brillouin zone [11]. In order to find the subband wave functions and subband energies the Hamiltonian [11] is transformed to eliminate the coupling between the spins with opposite direction in different valleys

$$H = \begin{bmatrix} H_1 & H_3 \\ H_3 & H_2 \end{bmatrix} \quad (1)$$

where H_1 , H_2 , and H_3 are written as

$$H_{1,2} = \left[\frac{\hbar^2 k_z^2}{2m_l} + \frac{\hbar^2 (k_x^2 + k_y^2)}{2m_t} + (-1)^j \delta + U(z) \right] I \quad (2)$$

$$H_3 = \begin{bmatrix} \frac{\hbar^2 k_0 k_z}{m_l} & 0 \\ 0 & \frac{\hbar^2 k_0 k_z}{m_l} \end{bmatrix} \quad (3)$$

Here $U(z)$ is the confinement potential, I is the identity 2×2 matrix, $\delta = \sqrt{\left(D\varepsilon_{xy} - \frac{\hbar^2 k_x k_y}{M}\right)^2 + \Delta_{SO}^2(k_x^2 + k_y^2)}$, m_t and m_l are the transversal and the longitudinal silicon effective masses, $M^{-1} \approx m_t^{-1} - m_0^{-1}$, $k_0 = 0.15 \times 2\pi/a$ is the position of the valley minimum relative to the X -point in unstrained silicon, ε_{xy} denotes the shear strain component, $D = 14\text{eV}$ is the shear strain deformation potential, and $\Delta_{SO} = 1.27\text{meVnm}$ is the effective spin-orbit interaction [8].

The surface roughness scattering matrix elements are taken to be proportional to the square of the product of the subband wave function derivatives at the interface [12,13]. The relaxation time τ can be calculated as a thermal average of the rates $\frac{1}{\tau(\mathbf{K})}$ [7,8,12].

This work is supported by the European Research Council through the grant #247056 MOSILSPIN.

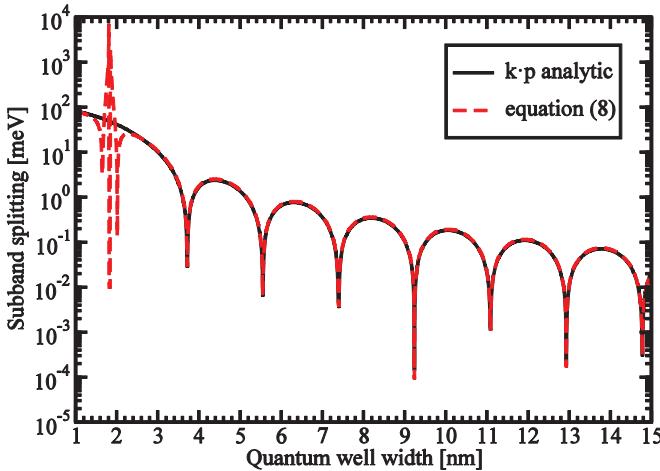


Figure 1. Splitting of the lowest unprimed electron subbands as a function of the silicon film thickness for the shear strain value of 0.5%, $k_x = 0.25\text{nm}^{-1}$, $k_y = 0.25\text{nm}^{-1}$.

$$\frac{1}{\tau} = \frac{\int \frac{1}{\tau(\mathbf{K}_1)} f(\varepsilon)(1-f(\varepsilon)) d\mathbf{K}_1}{\int f(\varepsilon) d\mathbf{K}_1} \quad (4)$$

$$f(\varepsilon) = \frac{1}{1 + \exp\left(\frac{\varepsilon - E_F}{k_B T}\right)} \quad (5)$$

$$\int d\mathbf{K}_1 = \int_0^{2\pi} \int_0^\infty \frac{|\mathbf{K}_1(\varphi, \varepsilon)|}{\left|\frac{\partial \varepsilon(\mathbf{K}_1)}{\partial \mathbf{K}_1}\right|} d\varphi d\varepsilon \quad (6)$$

ε is the electron energy, \mathbf{K}_1 is the in-plane wave vector after the scattering event, k_B is the Boltzmann constant, T is the temperature, and E_F is the Fermi level.

The surface roughness at the two interfaces is assumed to be independent and described by a mean value and a correlation length. The surface roughness induced momentum (spin) relaxation rate is calculated as

$$\begin{aligned} \frac{1}{\tau(\mathbf{K}_1)} &= \frac{2(4)\pi}{\hbar} \sum_{i,j=1,2} \int_0^{2\pi} \pi \Delta^2 L^2 \frac{1}{\epsilon_{ij}^2(\mathbf{K}_2 - \mathbf{K}_1)} \cdot \\ &\cdot \frac{\hbar^4}{4m_l^2} \left[\left(\frac{d\Psi_{i\mathbf{K}_1\sigma}}{dz} \right)^* \frac{d\Psi_{j\mathbf{K}_2\sigma(-\sigma)}}{dz} \right]_{z=\pm\frac{t}{2}}^2 \cdot \\ &\cdot \exp\left(\frac{-(\mathbf{K}_2 - \mathbf{K}_1)^2 L^2}{4}\right) \frac{|\mathbf{K}_2|}{\left|\frac{\partial \varepsilon(\mathbf{K}_2)}{\partial \mathbf{K}_2}\right|} \frac{1}{(2\pi)^2} d\varphi \end{aligned} \quad (7)$$

ϵ_{ij} is the dielectric permittivity, \mathbf{K}_2 is the in-plane wave vector of the state before scattering, L is the autocorrelation length, Δ is the mean square value of the surface roughness fluctuations, t is the film thickness, $\Psi_{i\mathbf{K}_1\sigma}$ and $\Psi_{j\mathbf{K}_2\sigma}$ are the wave vectors, σ is the spin projection to the [001] axis.

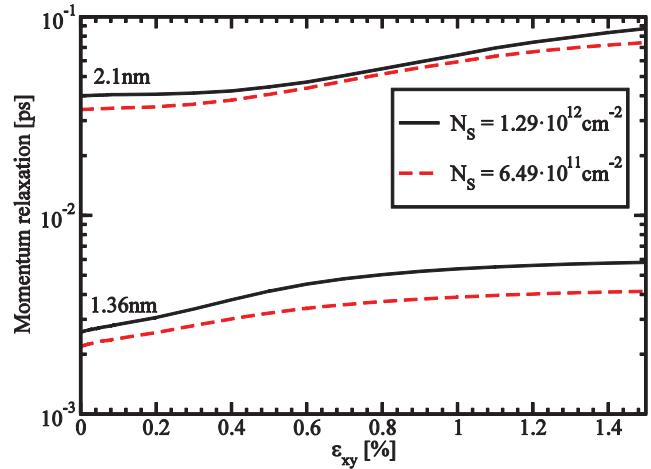


Figure 2. Dependence of the momentum relaxation time on shear strain for different film thicknesses and electron concentrations at $T=300\text{K}$.

RESULT AND DISCUSSION

The subband splitting dependence on the silicon film thickness is presented in Figure 1. Theoretical values for the subband splitting in an infinite potential square well [14] are:

$$\Delta E_n = \frac{2y_n^2 \delta}{k_0 t \sqrt{(1-y_n^2)(1-\eta^2)}} \left| \sin\left(\sqrt{\frac{1-y_n^2-\eta^2}{1-y_n^2}} k_0 t\right) \right| \quad (8)$$

with y_n , η , and B defined as

$$y_n = \frac{\pi n}{k_0 t} \quad (9)$$

$$\eta = \frac{m_l \delta}{\hbar^2 k_0^2} \quad (10)$$

$$\delta = \sqrt{\Delta_{so}^2(k_x^2 + k_y^2) + \left(D \varepsilon_{xy} - \frac{\hbar^2 k_x k_y}{M}\right)^2} \quad (11)$$

A good agreement between the $\mathbf{k}\cdot\mathbf{p}$ analytic and (8) is given for a film thicknesses over 2nm and below 1.5nm. A discrepancy appears only in a narrow interval around 1.81nm due to vanishing of the $(1-y_n^2)$ term.

Figure 2 shows the dependence of the momentum relaxation on shear strain. The improvement of the momentum relaxation time due to the shear strain is around 120% for the film thickness of 2.1nm and around 130% for the film thickness 1.36nm. We point out that the increase of the momentum relaxation time is due to the corresponding scattering matrix elements' dependences on strain shown in Figure 3. The intrasubband scattering matrix elements decrease with strain within the first (lowest) subband, which gets splits

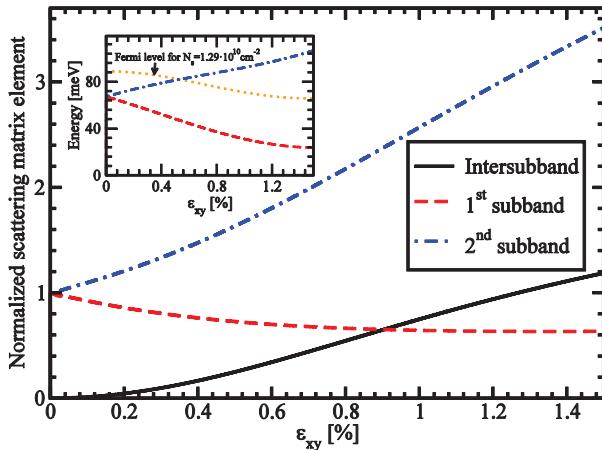


Figure 3. Inter- and intrasubband scattering matrix elements normalized to the value of the intrasubband scattering at zero strain as a function of shear strain. The inset shows the dependence of subband energies and Fermi levels on shear strain.

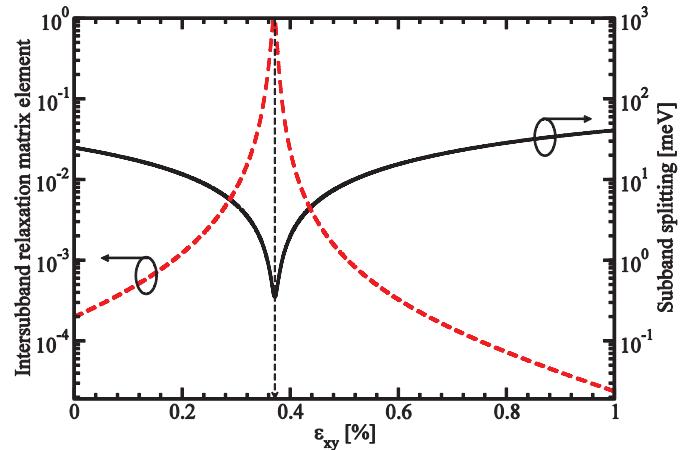


Figure 4. Intersubband relaxation matrix elements normalized to the intrasubband scattering elements at zero strain and the subband splitting for a film thickness of 2.48nm, $k_x = 0.4\text{nm}^{-1}$, and $k_y = 0.4\text{nm}^{-1}$.

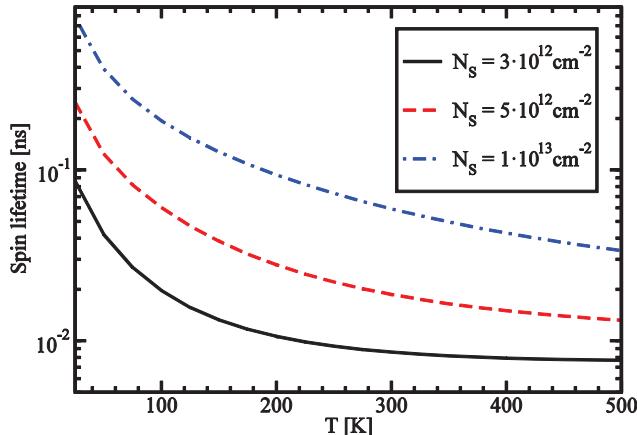


Figure 5. Dependence of the surface roughness limited spin lifetime on temperature for an unstrained film thickness of 2.48nm for different values of the electron concentration.

from the second one due to strain-induced valley splitting (inset in Figure 3). The occupation of the second subband at high strain becomes insignificant as confirmed by the Fermi level dependence (inset in Figure 3). Thus, the interplay of intrasubband scattering within the first subband and intersubband scattering mainly contribute to the momentum relaxation at high strain resulting in the overall increase of the momentum relaxation time (Figure 2). Combined with the strain induced transport effective mass decrease, it should result in an even better mobility improvement supporting the use of uniaxial tensile strain as the mobility booster in fully depleted ultra-thin SOI MOSFETs.

Figure 4 shows the lowest unprimed intersubband energy splitting as a function of shear strain. The surface roughness

spin relaxation matrix elements normalized to the scattering matrix elements at zero strain as a function of shear strain are also shown. The valley splitting is significantly reduced around a shear strain value of 0.37%. Since the intersubband valley splitting is proportional to the δ term, the position of the minimum is determined by the minimum of this term. At this point $D\epsilon_{xy} - \frac{\hbar^2 k_x k_y}{M}$ vanishes and the subband splitting is determined only by the spin-orbit term $\Delta_{SO}^2(k_x^2 + k_y^2)$. Thus, the spin-orbit interaction leads to a large mixing between the spin-up and spin-down states from the opposite valleys, resulting in narrow hot spots characterized by strong spin relaxation. For a fixed shear strain value the hot spots' positions are determined by the kinetic energy of the carrier. Figure 5 shows the dependence of the surface roughness induced spin lifetime on temperature for an unstrained film. While the temperature increases, the number of hot spots which lie in the energy range determined by the Fermi distribution increases, thus reducing the spin lifetime.

Figure 6 displays the normalized spin relaxation matrix elements for an unstrained film. The hot spots are located along the [100] and [010] directions. Shear strain moves the strong spin relaxation points to higher energies (Figure 7) outside of the states occupied by carriers. This leads to a reduction of the surface roughness induced spin relaxation.

A strong increase of the spin lifetime with shear strain at room temperature is demonstrated in Figure 8. For the film of 2.1nm thickness and the film of 1.36nm thickness the surface roughness induced spin relaxation increases by orders of magnitude with shear strain of 1.5% applied. Thus, uniaxial stress is efficient to boost the spin lifetime in ultra-thin SOI MOSFETs.

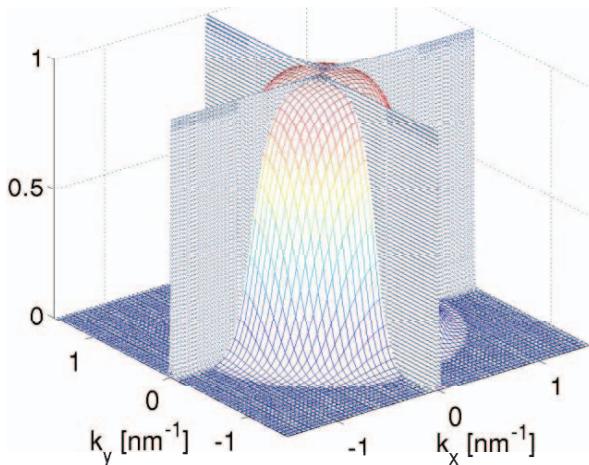


Figure 6. Intersubband relaxation matrix elements normalized to the intrasubband scattering elements at zero strain for an unstrained sample. The Fermi distribution for T=300K is also shown.

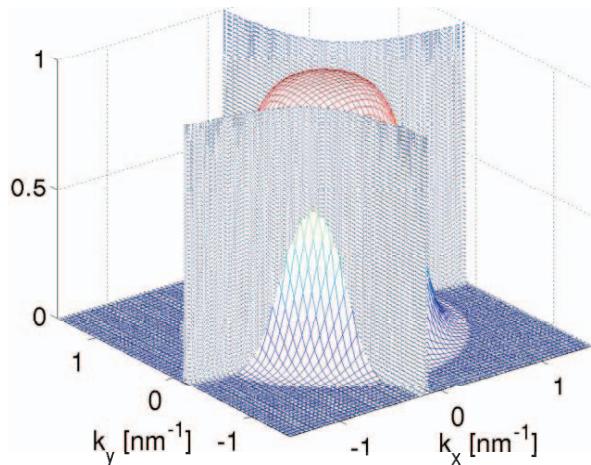


Figure 7. Normalized intersubband relaxation matrix elements for shear strain 0.5% shown together with the Fermi distribution at T=300K.

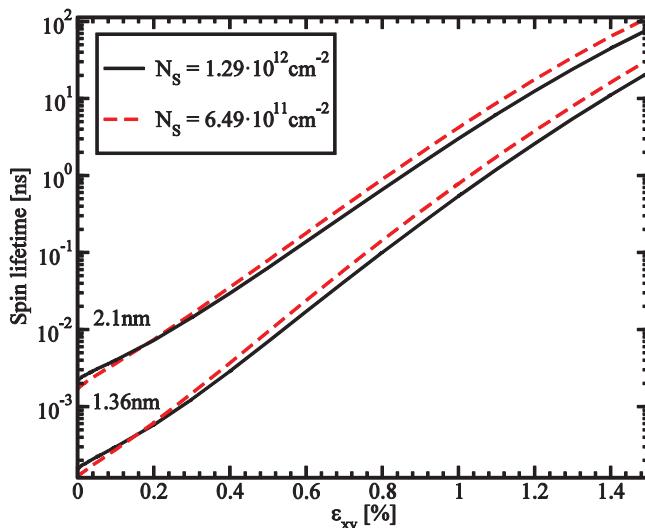


Figure 8. Dependence of the spin lifetime on shear strain for different film thicknesses and electron concentrations at T=300K.

III. CONCLUSION

By utilizing a $\mathbf{k} \cdot \mathbf{p}$ approach including the spin-orbit interaction effects we found the subband wave functions and subband energies in (001) thin silicon films. We have shown that the momentum relaxation time can be improved by almost a factor of two for ultra-thin films. We have demonstrated a strong, several orders of magnitude, increase of spin lifetime in strained silicon films. Thus shear strain used to boost mobility can also be used to increase spin lifetime.

ACKNOWLEDGMEN

The computational results have been achieved in part using the Vienna Scientific Cluster (VSC).

REFERENCES

- [1] S. Sugahara and J. Nitta, Spin transistor electronics: An overview and outlook, *Proceedings of the IEEE*, **98**(12), 2124–2154 (2010).
- [2] S. Datta and B. Das, Electronic analog of the electro-optic modulator, *Applied Physics Letters*, **56**, 665 (1990).
- [3] R. Jansen, Silicon spintronics, *Nature Materials*, **11**, 400–408 (2012).
- [4] B.Huang D.J. Monsma, and I. Appelbaum, Coherent spin transport through a 350 micron thick silicon wafer, *Physical Review Letters*, **99**, 177209 (2007).
- [5] C.H.Li, O.M.J. van 't Erve, and B.T. Jonker, Electrical injection and detection of spin accumulation in silicon at 500 K with magnetic metal/silicon dioxide contacts, *Nature Communications* **2**, 245 (2011).
- [6] L.Li, I.Appelbaum, Modeling spin transport in electrostatically-gated lateral-channel silicon devices: Role of interfacial spin relaxation, *Physical Review B*, **84**, 165318 (2011).
- [7] Y. Song, H. Dery, Analysis of phonon-induced spin relaxation processes in silicon, *Physical Review B*, **86**, 085201 (2012).
- [8] P. Li, H. Dery, Spin-orbit symmetries of conduction electrons in silicon, *Physical Review Letters*, **107**, 107203 (2011).
- [9] G.L. Bir, G.E. Pikus, Symmetry and strain-induced effects in semiconductors. New York/Toronto: J. Wiley & Sons 1974.
- [10] V. Sverdlov, Strain-induced effects in advanced MOSFETs. Wien - New York. Springer 2011.
- [11] D. Osintsev et al., Reduction of surface roughness induced spin relaxation in MOSFETs by strain, *Proceedings of the International Workshop on Computational Electronics*, (2012).
- [12] M. V. Fischetti et al., Six-band $\mathbf{k} \cdot \mathbf{p}$ calculation of hole mobility in silicon inversion layers: Dependence on surface orientation, strain, and silicon thickness, *Journal of Applied Physics*, **94**, 1079 (2003).
- [13] S. Jin, M.V. Fischetti, T.-W. Tang, Modeling of surface roughness scattering in ultrathin-body SOI MOSFETs, *IEEE Transaction Electron Devices*, **54**(9), 2191–2202 (2007).
- [14] D.Osintsev et al., Subband spitting and surface roughness induced spin relaxation in (001) silicon SOI MOSFETs, *Solid-State Electronics*, 10.1016/j.sse.2013.02.055, (2013).