

## NUMERICAL SIMULATION OF SEMICONDUCTOR DEVICES

Siegfried Selberherr and Erasmus Langer  
Institute for Microelectronics  
Technical University Vienna  
A-1040 Wien / Austria

### ABSTRACT

The state of the art in self-consistent numerical modeling of semiconductor devices is reviewed. The physical assumptions which are required to describe carrier transport are discussed. Particular emphasis is put on the models for space charge, carrier mobility, carrier temperature, and carrier generation-recombination. Investigations about three-dimensional effects due to the field oxide in MOS-devices analyzed with MINIMOS 5 are presented.

### INTRODUCTION

Device Modeling based on the self-consistent solution of fundamental semiconductor equations dates back to the famous work of Gummel in 1964. Though since that time there has been a continuous progress on that field the shrinking dimensions of the elements of integrated circuits require even more suitable device models in view of physics and mathematics for accurate simulation. On the one hand more and more sophisticated physical models are needed (even for low ambient temperature applications [13], [17]) and on the other hand a great demand for three-dimensional simulation tools has appeared as usual two-dimensional device simulation is not applicable satisfactorily to narrow channel devices. Modern strategies for the application of simulation can be found in [4], [28].

### PHYSICAL ASPECTS

#### Basic Equations

The model for hot carrier transport used in any numerical device simulation is based on the well known fundamental semiconductor equations (1)-(5). There are ongoing arguments in the scientific community whether these equations are adequate to describe transport in submicron devices. Particularly the current relations (4) and (5) which are the most complex equations out of the set of the basic semiconductor device equations undergo strong criticism in view of, for instance, ballistic transport [23], [35]. Their derivation from more fundamental physical principles is indeed not at all straightforward. They appear therefore with all sorts of slight variations in the specialized literature and a vast number of papers has been published where some of their subtleties are dealt with. Anyway, recent investigations on ultra short MOSFET's [37] do not give evidence that it is necessary to waive these well established basic equations for silicon devices down to feature sizes in the order of 0,1 microns [43]. Other approaches for semiconductor device simulation are based on the method of generalized stream functions [46] or on the Monte Carlo method [39].

$$\operatorname{div}(\varepsilon \cdot \operatorname{grad} \psi) = -\rho \quad (1)$$

$$\operatorname{div} \vec{J}_n - q \cdot \frac{\partial n}{\partial t} = q \cdot R \quad (2)$$

$$\operatorname{div} \vec{J}_p + q \cdot \frac{\partial p}{\partial t} = -q \cdot R \quad (3)$$

$$\vec{J}_n = q \cdot \mu_n \cdot n \cdot \left( \vec{E} + \frac{1}{n} \cdot \operatorname{grad} \left( n \cdot \frac{k \cdot T_p}{q} \right) \right) \quad (4)$$

$$\vec{J}_p = q \cdot \mu_p \cdot p \cdot \left( \vec{E} - \frac{1}{p} \cdot \operatorname{grad} \left( p \cdot \frac{k \cdot T_n}{q} \right) \right) \quad (5)$$

These equations include a set of parameters which have to be appropriately modeled in order to describe the various transport phenomena qualitatively and quantitatively correctly.

#### Modeling Space Charge

Poisson's equation (1) requires a model for the space charge  $\rho$  which makes use of only the dependent variables  $\psi, n, p$  and material properties. The well established approach for this model is to sum up the concentrations with the adequate charge sign multiplied with the elementary charge (6).

$$\rho = q \cdot (p - n + N_D^+ - N_A^-) \quad (6)$$

Here a difference between room temperature and low temperature simulation becomes apparent. The doping concentration is usually assumed to be fully ionized at room temperature which intuitively does not hold for low temperature analysis. The classical way to describe partial ionization is based on the formulae (7).

$$N_D^+ = \frac{N_D}{1 + 2 \cdot \exp\left(\frac{E_{fn} - E_D}{k \cdot T}\right)} \quad (7)$$

$$N_A^- = \frac{N_A}{1 + 4 \cdot \exp\left(\frac{E_A - E_{fp}}{k \cdot T}\right)}$$

$E_D$  and  $E_A$  are the ionization energies of the respective donor and acceptor dopant. A quite complete list of values can be found in [44]. These ionization energies are recommended to be modeled doping dependent in [12], however, it seems not to be important for MOSFET's regarding my experience. The Fermi levels  $E_{fn}$  and  $E_{fp}$  have to be appropriately related to the dependent variables.

$$\frac{E_{fn} - E_D}{k \cdot T} = G_{1/2} \left( \frac{n}{N_c} \right) + \frac{E_c - E_D}{k \cdot T} \quad (8)$$

$$\frac{E_A - E_{fp}}{k \cdot T} = G_{1/2} \left( \frac{p}{N_v} \right) + \frac{E_A - E_v}{k \cdot T} \quad (9)$$

$G_{1/2}(x)$  is the inverse Fermi function of order 1/2 defined with (10).

$$G_{1/2} \left( \frac{2}{\sqrt{\pi}} \cdot \int_0^{\infty} \frac{\sqrt{y}}{1 + e^{y-x}} \cdot dy \right) = x \quad (10)$$

$N_c$  and  $N_v$  are the density of states in the conduction band and the valence band, respectively. The ratio of the density of states depends only on the ratio of the effective masses of electrons and holes. The product can be fitted to measured data of the intrinsic concentration requiring only models, e.g. [9], for the effective masses and the energy gap. With these two relations it is straightforward to compute the numerical values for the density of states. A full derivation of this partial ionization model can be found in [42].

### Modeling Carrier Mobilities

The next set of physical parameters to be considered carefully for low temperature simulation consists of the carrier mobilities  $\mu_n$  and  $\mu_p$  in (4) and (5). The models for the carrier mobilities have to take into account a great variety of scattering mechanisms the most basic one of which is lattice scattering. The lattice mobility in pure silicon can be fitted with simple power laws.

$$\mu_n^L = 1430 \frac{\text{cm}^2}{\text{Vs}} \cdot \left( \frac{T}{300\text{K}} \right)^{-2}, \quad \mu_p^L = 460 \frac{\text{cm}^2}{\text{Vs}} \cdot \left( \frac{T}{300\text{K}} \right)^{-2,18} \quad (11)$$

The expressions (11) fit well experimental data of [3], [10] and [27].

The next effect to be considered is ionized impurity scattering. The best established procedure for this task is to take the functional form (12) of the fit provided by Caughey and Thomas [11] and use temperature dependent coefficients.

$$\mu_{n,p}^{LI} = \mu_{n,p}^{\min} + \frac{\mu_{n,p}^L - \mu_{n,p}^{\min}}{1 + \left( \frac{CI}{C_{n,p}^{ref}} \right)^{\alpha_{n,p}}} \quad (12)$$

$$\mu_n^{\min} = \begin{cases} 80 \frac{\text{cm}^2}{\text{Vs}} \cdot \left( \frac{T}{300\text{K}} \right)^{-0,45} & T \geq 200\text{K} \\ 80 \frac{\text{cm}^2}{\text{Vs}} \left( \frac{200\text{K}}{300\text{K}} \right)^{-0,45} \cdot \left( \frac{T}{200\text{K}} \right)^{-0,15} & T < 200\text{K} \end{cases} \quad (13)$$

$$\mu_p^{\min} = \begin{cases} 45 \frac{\text{cm}^2}{\text{Vs}} \cdot \left( \frac{T}{300\text{K}} \right)^{-0,45} & T \geq 200\text{K} \\ 45 \frac{\text{cm}^2}{\text{Vs}} \left( \frac{200\text{K}}{300\text{K}} \right)^{-0,45} \cdot \left( \frac{T}{200\text{K}} \right)^{-0,15} & T < 200\text{K} \end{cases} \quad (14)$$

$$C_n^{ref} = 1,12 \cdot 10^{17} \text{cm}^{-3} \cdot \left( \frac{T}{300\text{K}} \right)^{3,2}, \quad C_p^{ref} = 2,23 \cdot 10^{17} \text{cm}^{-3} \cdot \left( \frac{T}{300\text{K}} \right)^{3,2} \quad (15)$$

$$\alpha_{n,p} = 0,72 \cdot \left( \frac{T}{300\text{K}} \right)^{0,065} \quad (16)$$

The fits (13)-(16) are from [22]. Similar data have been provided in [6] and [16].

In view of partial ionization one should consider neutral impurity scattering [41]. However, in view of the uncertainty of the quantitative values for ionized impurity scattering it seems not to be worthwhile to introduce another scattering mechanism with additional fitting parameters. Furthermore, partial ionization appears to be a second order effect even at liquid nitrogen temperature. It seems therefore justified to include partial ionization only in the space charge model and not in the carrier mobilities.

Particular emphasis has to be put on surface scattering which we model with an expression suggested by Seavey [40].

$$\mu_{n,p}^{LIS} = \frac{\mu_{n,p}^{ref} + (\mu_{n,p}^{LI} - \mu_{n,p}^{ref}) \cdot (1 - F(y))}{1 + F(y) \cdot \left( \frac{S_{n,p}}{S_{n,p}^{ref}} \right)^{\alpha_{n,p}}} \quad (17)$$

$$\mu_n^{ref} = 638 \frac{\text{cm}^2}{\text{Vs}} \cdot \left( \frac{T}{300\text{K}} \right)^{-1,19}, \quad \mu_p^{ref} = 160 \frac{\text{cm}^2}{\text{Vs}} \cdot \left( \frac{T}{300\text{K}} \right)^{-1,09}$$

$$F(y) = \frac{2 \cdot \exp \left( - \left( \frac{y}{y^{ref}} \right)^2 \right)}{1 + \exp \left( -2 \cdot \left( \frac{y}{y^{ref}} \right)^2 \right)} \quad (18)$$

$$S_n = \max \left( 0, \frac{\partial \psi}{\partial y} \right), \quad S_p = \max \left( 0, - \frac{\partial \psi}{\partial y} \right)$$

$S_n^{ref}$  is assumed to be  $7 \cdot 10^5 \frac{\text{V}}{\text{cm}}$ ;  $S_p^{ref}$  is  $2,7 \cdot 10^5 \frac{\text{V}}{\text{cm}}$  and  $y^{ref}$  is 10nm.

The formulae for surface scattering are definitely not the ultimate expressions. They just fit quite reasonably experimental observations. Other approaches with the same claim can be found in, e.g., [5], [24], [31], [36]. A u-shaped mobility behavior as found in [8] has not been synthesized because we believe in a different origin than surface scattering for this experimental observation.

### Modeling Carrier Temperatures

To describe carrier heating properly one has to account for local carrier temperatures  $T_{n,p}$  in the current relations (4) and (5) [20], [38], [47]. This can be achieved by either solving energy conservation equations self consistently with the basic transport equations [7], [30], or by using a model obtained by series expansions of the solution to the energy conservation equations [21]. We believe that the latter generally is sufficient for silicon devices although energy transport simulation will gain particular relevance for bipolar devices with shallow emitters [33].

$$U_{t_{n,p}} = \frac{k \cdot T_{n,p}}{q} = U_{t_0} + \frac{2}{3} \cdot \tau_{n,p}^{\epsilon} \cdot (v_{n,p}^{sat})^2 \cdot \left( \frac{1}{\mu_{n,p}^{LISE}} - \frac{1}{\mu_{n,p}^{LIS}} \right) \quad (19)$$

For the electronic voltages we have (19) as an approximation. Confirming theoretical investigations can be found in [1].

The energy relaxation times  $\tau_{n,p}^e$  are in the order of 0,5 picoseconds and just weakly temperature dependent [9]. They should however be modeled as functions of the local doping concentration as motivated by the following reasoning. The product of carrier mobility times electronic voltage which symbolizes a diffusion coefficient must be a decreasing function with increasing carrier voltage (see also [9]). Its maximum is attained at thermal equilibrium. Relation (20) must therefore hold.

$$\mu_{n,p}^{LISE} \cdot U t_{n,p} \leq \mu_{n,p}^{LIS} \cdot U t_o \quad (20)$$

Note that models for carrier diffusion coefficients are not required in the basic current relations (4), (5). Substituting (19) into (20) and rearranging terms one obtains relation (21) for the energy relaxation times.

$$\tau_{n,p}^e \leq \frac{3}{2} \cdot U t_o \cdot \frac{\mu_{n,p}^{LIS}}{(v_{n,p}^{sat})^2} \quad (21)$$

In MINIMOS 5 [45] the energy relaxation times are modeled on the basis of (21) with a fudge factor  $\gamma$  in the range [0,1] and a default value of 0,8.

$$\tau_{n,p}^e = \gamma \cdot \frac{3}{2} \cdot U t_o \cdot \frac{\mu_{n,p}^{LIS}}{(v_{n,p}^{sat})^2} \quad (22)$$

For vanishing doping one obtains the maximal energy relaxation times which are at 300K  $\tau_n^e = 4,44 \cdot 10^{-13}$ s,  $\tau_p^e = 2,24 \cdot 10^{-13}$ s and at 77K  $\tau_n^e = 8,82 \cdot 10^{-13}$ s,  $\tau_p^e = 8,68 \cdot 10^{-13}$ s.

#### Modeling Carrier Generation/Recombination

An adequate model for thermal generation/recombination even for low temperature can be found in [41]. A comment should be made on the model for the impact ionization rate which has to be supplied for the continuity equations (2) and (3). It still seems, though under heavy dispute in the scientific community, that the old Chynoweth formulation (23) of impact ionization can be used quite satisfactorily for device simulation.

$$R^{II} = -\alpha_n \cdot \frac{|\vec{J}_n|}{q} - \alpha_p \cdot \frac{|\vec{J}_p|}{q} \quad (23)$$

with:

$$\alpha_{n,p} = \alpha_{n,p}^\infty \cdot \exp\left(-\frac{\beta_{n,p}}{E}\right) \quad (24)$$

The coefficients of (24) can be modeled temperature dependent by (25) and (26) to fit experimental data [14], [15], [32]. It should be noted that there is some lack of data for liquid nitrogen temperature, cf. [43]. However it seems that this impact ionization model is probably somewhat too pessimistic for a proper quantitative prediction of substrate currents as already stated in [26], [42].

$$\alpha_n^\infty = 7 \cdot 10^5 \text{cm}^{-1} \cdot \left(0,57 + 0,43 \cdot \left(\frac{T}{300\text{K}}\right)^2\right) \quad (25)$$

$$\alpha_p^\infty = 1,58 \cdot 10^6 \text{cm}^{-1} \cdot \left(0,58 + 0,42 \cdot \left(\frac{T}{300\text{K}}\right)^2\right)$$

$$\beta_n = 1,23 \cdot 10^6 \frac{\text{V}}{\text{cm}} \cdot \left(0,625 + 0,375 \cdot \left(\frac{T}{300\text{K}}\right)\right) \quad (26)$$

$$\beta_p = 2,04 \cdot 10^6 \frac{\text{V}}{\text{cm}} \cdot \left(0,67 + 0,33 \cdot \left(\frac{T}{300\text{K}}\right)\right)$$

The Auger coefficients for the model of Auger recombination (27) can also be made weakly temperature dependent with (28). The fit has been made to the data of [18].

$$R^{AU} = (C_{cn} \cdot n + C_{cp} \cdot p) \cdot (n \cdot p - n_i^2) \quad (27)$$

$$C_{cn} = 2,8 \cdot 10^{-31} \frac{\text{cm}^6}{\text{s}} \cdot \left(\frac{T}{300\text{K}}\right)^{0,14}, \quad (28)$$

$$C_{cp} = 9,9 \cdot 10^{-32} \frac{\text{cm}^6}{\text{s}} \cdot \left(\frac{T}{300\text{K}}\right)^{0,2}$$

#### NUMERICAL ASPECTS

The numerical solution of the semiconductor equations, i.e. discretization, linearization, and solving the resulting algebraic equation system, can not be discussed here in detail. The interested reader is referred to [29], [34]. Recent investigations of numerical algorithms can be found in [48]. The use of more general transport equations require subtle modification to the numerics, an example of which is given in [19].

#### A GLIMPSE OF RESULTS

This chapter presents three-dimensional effects of MOSFET's due to the nonplanar nature of the field-oxide body. The investigations have been carried out by MINIMOS 5 [45] which accounts for all three spatial dimensions. Three-dimensional effects like threshold shift for small channel devices, channel narrowing and the enhanced conductivity at the channel edge have been successfully modeled. Similar investigations leading to matching results have been performed in [2], [25].

The geometry of the investigated 3-D MOSFET is given in Fig. 1: an n-channel with an  $1\mu\text{m} \times 1\mu\text{m}$  channel and gate oxide of 15nm. The oxide body of the analyzed structure can be seen in Fig. 2 (note that the oxide is between the upper and the lower plane).

In order to demonstrate the effects at the channel edge we select two different bias points. The first is near threshold with  $U_S = U_B = 0.0\text{V}$ ,  $U_{DS} = 1.0\text{V}$ ,  $U_{GS} = 0.5\text{V}$  (the threshold voltage for this device is  $U_{th} \sim 0.75\text{V}$ ). The potential distribution in channel length and width direction at the semiconductor/gate-oxide interface is shown in Fig. 3. (This plane penetrates into the field-oxide near the contact boundary of source and drain.) The corresponding minority carrier distribution is given in Fig. 4. A remarkable depletion region at the drain side causes the channel charge to be smaller (under certain bias conditions) than predicted by 2-D simulations.

The second bias point is far above threshold  $U_S = U_B = 0.0\text{V}$ ,  $U_{DS} = 1.0\text{V}$ ,  $U_{GS} = 3.0\text{V}$ . The corresponding potential distribution can be seen in Fig. 5. The location of the plane which the distribution is drawn for, is the same as at the previous bias condition. The high increase of the potential distribution out of the channel is due to the gate contact overlapping the

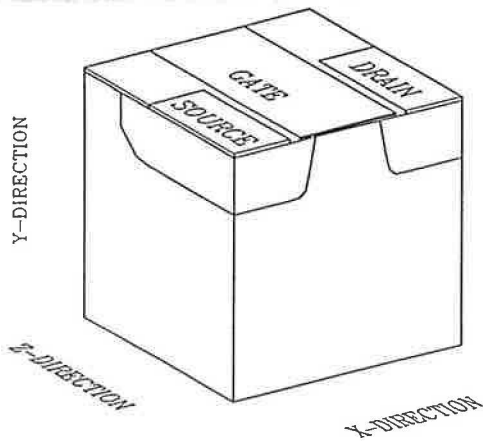


Fig.1: Investigated 3-D MOSFET structure

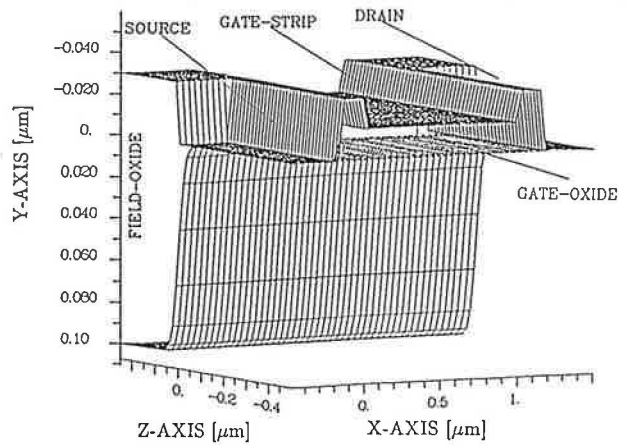


Fig.2: Oxide body of the structure

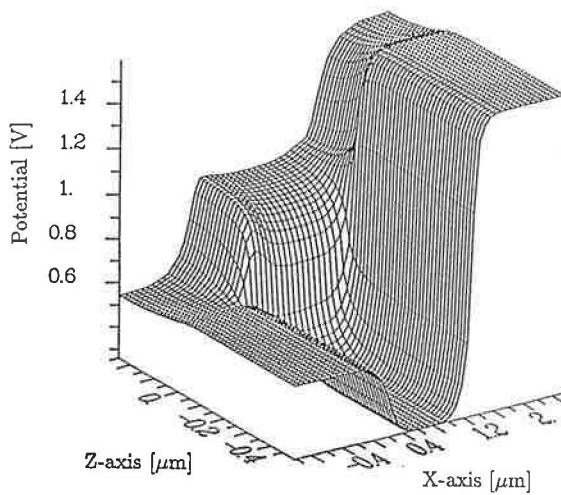


Fig.3: Detailed view of the surface potential at bias  $U_{GS} = 0.5\text{V}$

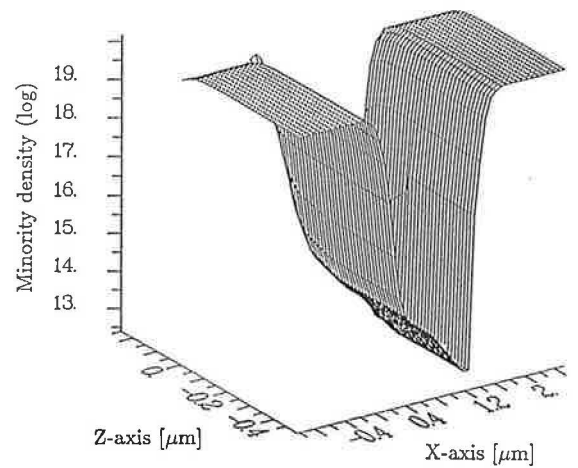


Fig.4: Detailed view of the minority carrier density at bias  $U_{GS} = 0.5\text{V}$

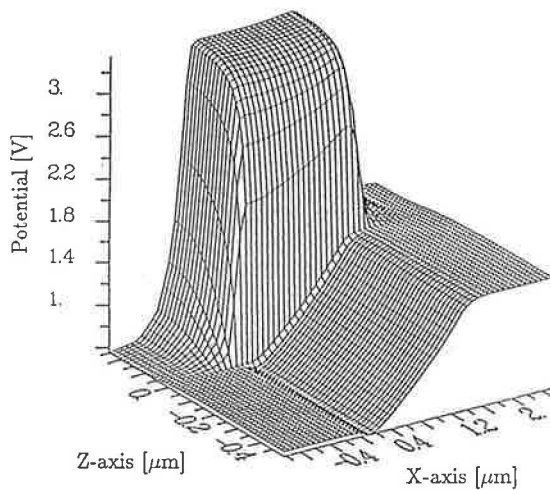


Fig.5: Detailed view of the surface potential at bias  $U_{GS} = 3.0\text{V}$

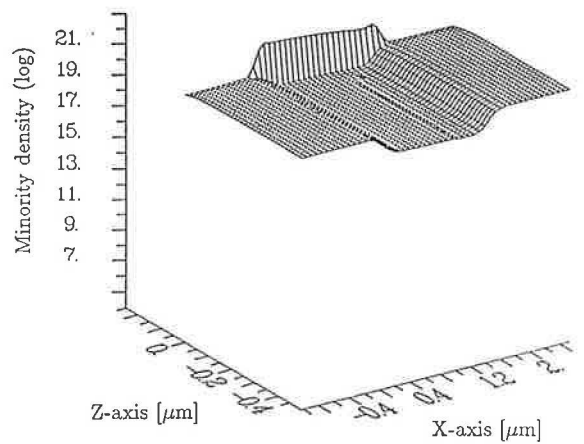


Fig.6: Detailed view of the minority carrier density at bias  $U_{GS} = 3.0\text{V}$

field-oxide. Also interesting is the minority carrier distribution (Fig. 6) which shows the enhanced conductivity at the semiconductor field-oxide interface. Note that only one half of the channel width is shown in Fig. 3 - Fig. 6;  $-0.5\mu\text{m}$  denotes the middle of the channel width and  $0.0\mu\text{m}$  the boundary of source and drain contacts. The consequence on the device characteristics of these effects depends on the gradient of the "bird's beak" and the channel width. A high gradient in the field-oxide shape results in high parasitic current at the channel edge; this effect is less significant for low gradients. Narrow channel devices with high gradient have much higher currents than predicted by 2-D calculations while the agreement with 2-D simulations is good for wide channel devices in any case. Using a low gradient in bird's beak yields a very smooth potential distribution compared to a nearly rectangular shape.

Fig. 7 shows a comparison of two-dimensional and three-dimensional characteristics for  $U_{DS} = 1\text{V}$  and a rectangular approximated field-oxide.

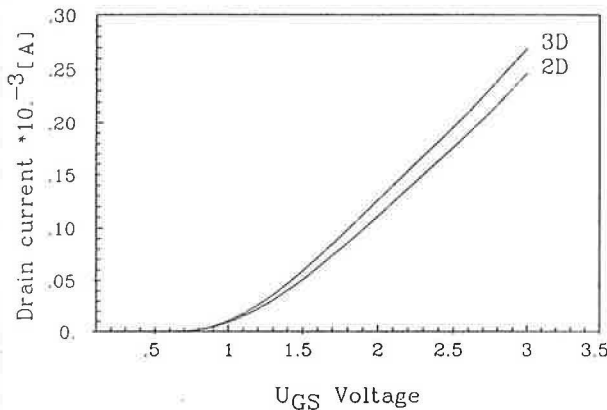


Fig.7: 2-D and 3-D characteristic at bias  $U_{DS} = 1.0\text{V}$

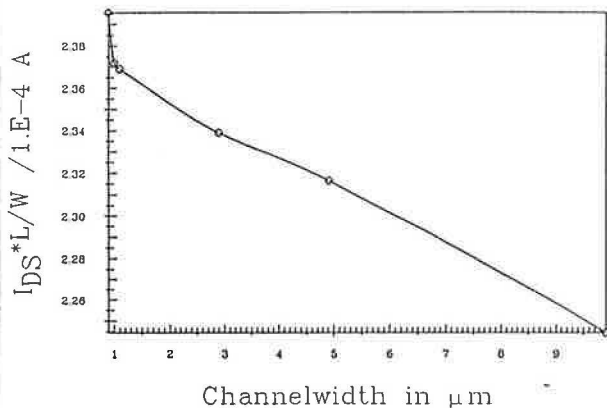


Fig.8: Measured dependence of the drain current on the channel width

The dependence of the drain current of n-channel devices with weak field implantation on the channel width is shown in Fig. 8. The marked points indicate the measured transistors which have been investigated at the same bias conditions where the enhanced conductivity can be seen.

#### ACKNOWLEDGMENT

This work is considerably supported by the research laboratories of SIEMENS AG at Munich, FRG, the research laboratories of DIGITAL EQUIPMENT CORPORATION at Hudson, USA, and the "Fond zur Förderung der wissenschaftlichen Forschung" under contract S43/10. We are indebted to Prof. H. Pötlz for many critical and stimulating discussions.

#### REFERENCES

- [1] Ahmad N., Arora V.K., "Velocity-Field Profile of n-Silicon: A Theoretical Analysis", *IEEE Trans. Electron Devices*, Vol.ED-33, pp.1075-1077, 1986.
- [2] Akers L.A., Sugino M., Ford J.M., "Characterization of the Inverse-Narrow-Width Effect", *IEEE Trans. Electron Devices*, Vol.ED-34, pp.2476-2484, 1987.
- [3] Ali-Omar M., Reggiani L., "Drift and Diffusion of Charge Carriers in Silicon and Their Empirical Relation to the Electric Field", *Solid-State Electron.*, Vol.30, pp.693-697, 1987.
- [4] Alvarez A.R. et al., "Application of Statistical Design and Response Surface Methods to Computer-Aided VLSI Device Design", *IEEE Trans. CAD*, Vol.7, pp.272-288, 1988.
- [5] Arora N.D., Gildenblat G.S., "A Semi-Empirical Model of the MOSFET Inversion Layer Mobility for Low-Temperature Operation", *IEEE Trans. Electron Devices*, Vol.ED-34, pp.89-93, 1987.
- [6] Arora N.D., Hauser J.R., Roulston D.J., "Electron and Hole Mobilities in Silicon as a Function of Concentration and Temperature", *IEEE Trans. Electron Devices*, Vol.ED-29, pp.292-295, 1982.
- [7] Azoff E.M., "Generalized Energy-Momentum Conservation Equations in the Relaxation Time Approximation", *Solid-State Electron.*, Vol.30, pp.913-917, 1987.
- [8] Baccarani G., Wordeman M.R., "Transconductance Degradation in Thin-Oxide MOSFET's", *IEEE Trans. Electron Devices*, Vol.ED-30, pp.1295-1304, 1983.
- [9] Baccarani G., Wordeman M.R., "An Investigation of Steady-State Velocity Overshoot in Silicon", *Solid-State Electron.*, Vol.28, pp.407-416, 1985.
- [10] Canali C., Majni G., Minder R., Ottaviani G., "Electron and Hole Drift Velocity Measurements in Silicon and Their Empirical Relation to Electric Field and Temperature", *IEEE Trans. Electron Devices*, Vol.ED-22, pp.1045-1047, 1975.
- [11] Caughey D.M., Thomas R.E., "Carrier Mobilities in Silicon Empirically Related to Doping and Field", *Proc.IEEE*, Vol.52, pp.2192-2193, 1967.
- [12] Chrzanoska-Jeske M., Jaeger R.C., "Modeling of Temperature Dependent Transport Parameters for Low Temperature Bipolar Transistor Simulation", *The Electrochemical Society*, Proc.Vol.88-9, pp.30-38, 1988.

- [13] Colonna-Romano L.M., Deverell D.R., "Operation of a CMOS Microprocessor While Immersed in Liquid Nitrogen", *IEEE J.Solid-State Circuits*, Vol.SC-21, pp.491-492, 1986.
- [14] Crowell C.R., Sze S.M., "Temperature Dependence of Avalanche Multiplication in Semiconductors", *Appl.Phys. Lett.*, Vol.9, pp.242-244, 1966.
- [15] Decker D.R., Dunn C.N., "Temperature Dependence of Carrier Ionization Rates and Saturated Velocities in Silicon", *J.Electronic Mat.*, Vol.4, pp.527-547, 1975.
- [16] Dorkel J.M., Leturcq Ph., "Carrier Mobilities in Silicon Semi-Empirically Related to Temperature, Doping and Injection Level", *Solid-State Electron.*, Vol.24, pp.821-825, 1981.
- [17] Duke D.W., "Use of the ETA-10 Supercomputer: A Status Report", *The Electrochemical Soc.*, Proc.Vol.88-9, pp.30-38, 1988.
- [18] Dzierwior J., Schmid W., "Auger Coefficients for Highly Doped and Highly Excited Silicon", *Appl. Phys. Lett.*, Vol.31, pp.346-348, 1977.
- [19] Forghieri A. et al., "A New Discretization Strategy of the Semiconductor Equations Comprising Momentum and Energy Balance", *IEEE Trans. CAD*, Vol.7, pp.231-242, 1988.
- [20] Hänsch W., Orlowski M., Weber W., "The hot-electron problem in submicron MOSFET", *Proc. ESSDERC '88 Montpellier France*, 1988.
- [21] Hänsch W., Selberherr S., "MINIMOS 3: A MOSFET Simulator that Includes Energy Balance", *IEEE Trans. Electron Devices*, Vol.ED-34, pp.1074-1078, 1987.
- [22] Henning A.K. et al., "Substrate Current at Cryogenic Temperatures: Measurements and a Two-Dimensional Model for CMOS Technology", *IEEE Trans. Electron Devices*, Vol.ED-34, pp.64-74, 1987.
- [23] Hess K., Iafrate G.J., "Theory and Applications of Near Ballistic Transport in Semiconductors", *Proc.IEEE*, Vol.76, pp.519-532, 1988.
- [24] Hiroki A. et al., "A Mobility Model for Submicrometer MOSFET Device Simulations", *IEEE Electron Device Lett.*, Vol.EDL-8, pp.231-233, 1987.
- [25] Hsueh K.K., Sanchez J.J., Demassa T.A., Akers L.A., "Inverse-Narrow-Width Effects and Small-Geometry MOSFET Threshold Voltage Model", *IEEE Trans. Electron Devices*, Vol.35, pp.325-338, 1988.
- [26] Lau D., Goldenblat G., Sodini G.G., Nelson D.E., "Low-Temperature Substrate Current Characterization of n-Channel MOSFET's", *Proc.IEDM*, pp.565-568, 1985.
- [27] Li S.S., Thurber W.R., "The Dopant Density and Temperature Dependence of Electron Mobility and Resistivity in n-Type Silicon", *Solid-State Electron.*, Vol.20, pp.609-616, 1977.
- [28] Marash V., Dutton R.W., "Methodology for Submicron Device Model Development", *IEEE Trans. CAD*, Vol.7, pp.299-306, 1988.
- [29] Markowich P.A., *The Stationary Semiconductor Device Equations*, Springer, Wien New-York, 1986.
- [30] Meinerzhagen B., Engl W.L., "The Influence of the Thermal Equilibrium Approximation on the Accuracy of Classical Two-Dimensional Numerical Modeling of Silicon Submicrometer MOS Transistors", *IEEE Trans. Electron Devices*, Vol.35, pp.689-697, 1988.
- [31] Nishida T., Sah C.T., "A Physically Based Mobility Model for MOSFET Numerical Simulation", *IEEE Trans. Electron Devices*, Vol.ED-34, pp.310-320, 1987.
- [32] Okuto Y., Crowell C.R., "Threshold Energy Effect on Avalanche Breakdown Voltage in Semiconductor Junctions", *Solid-State Electron.*, Vol.18, pp.161-168, 1975.
- [33] Ou H., Tang T., "Numerical Modeling of Hot Carriers in Submicrometer Silicon BJT's", *IEEE Trans. Electron Devices*, Vol.ED-34, pp.1533-1539, 1987.
- [34] Polak S.J., Den Heijer C., Schilders W.H.A., Markowich P., "Semiconductor Device Modelling from the Numerical Point of View", *Internat.J.Numer.Methods in Engineering*, Vol.24, pp.763-838, 1987.
- [35] Robertson P.J., Dumin D.J., "Ballistic Transport and Properties of Submicrometer Silicon MOSFET's from 300 to 4.2K", *IEEE Trans. Electron Devices*, Vol.ED-33, pp.494-498, 1986.
- [36] Rothwarf A., "A New Quantum Mechanical Channel Mobility Model for Si MOSFET's", *IEEE Electron Device Letters*, Vol.EDL-8, pp.499-502, 1987.
- [37] Sai-Halasz G.A., "Processing and Characterization of Ultra Small Silicon Devices", *Proc.ESSDERC Conf.*, pp.71-80, 1987.
- [38] Sangiorgi E., Pinto M.R., Venturi F., Fichtner W., "A Hot Carrier Analysis of Submicron MOSFET's", *Technical Report AT&T Bell Laboratories*, No.87/8, 1987.
- [39] Sangiorgi E., Ricco B., Venturi F., "MOS<sup>2</sup>: An Efficient Monte Carlo Simulator for MOS Devices", *IEEE Trans. CAD*, Vol.7, pp.259-271, 1988.
- [40] Seavey M., Private Communication, 1987.
- [41] Selberherr S., *Analysis and Simulation of Semiconductor Devices*, Springer, Wien New-York, 1984.
- [42] Selberherr S., "Low Temperature Mos Device Modeling", *The Electrochemical Soc.*, Proc.Vol.88-9, pp.43-86, 1988.
- [43] Shahidi G.G., Antoniadis D.A., Smith H.L., "Electron Velocity Overshoot at 300K and 77K in Silicon MOSFET's with Submicron Channel Length", *Proc.IEDM*, pp.824-825, 1986.
- [44] Sze S.M., *Physics of Semiconductor Devices*, Wiley, New York, 1969.
- [45] Thurner M., Selberherr S., "The Extension of MINIMOS to a 3-D Simulation Program", *Proc. NASECODE V Conf.*, pp.327-332, 1987.
- [46] Williams R.A., Pattanayak D.N., "ADAM: A Two-Dimensional, Two-Carrier MOSFET Simulator Based on Generalized Stream Functions", *IEEE Trans. CAD*, Vol.7, pp.243-250, 1988.
- [47] Wilson C.L., "Hydrodynamic Carrier Transport in Semiconductors with Multiple Band Minima", *IEEE Trans. Electron Devices*, Vol.35, pp.180-187, 1988.
- [48] Yoshii A., Tomizawa M., Yokoyama K., "Investigation of Numerical Algorithms in Semiconductor Device Simulation", *Solid-State Electron.*, Vol.30, pp.813-820, 1987.