

A Model for Switching Traps in Amorphous Oxides

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Abstract—Negative Bias Temperature Instability (NBTI) is frequently suspected to arise from a delicate interplay between some sort of hole trapping and an interface generation mechanism. In a recently suggested model the E' center along with its second form as an Si–Si dimer are supposed to play a key role. Despite of its successful application to a large amount of experimental data, this model relies on a classical determination of the bandedge energy diagram and the carrier concentrations. The occurrence of subbands in the inversion layer shifts the initial energy level for charge trapping and may thus strongly impact the trapping dynamics. We evaluate the new model against measurement data in order to investigate the impact of quantization effects on the model parameters.

I. INTRODUCTION

Negative Bias Temperature Instability (NBTI) still ranks among the most serious reliability issues in present-day semiconductor devices. Even though a series of models have been devised to explain NBTI, none of them have so far been capable of reproducing the experimentally observed touchstones, such as the field- and temperature-dependence as well as the asymmetry between stress and relaxation. A recently proposed mechanism [1], involving charge trapping according to lattice relaxation multiphonon emission (LRME), seems to overcome these difficulties. The model relies on the well-established picture of switching traps, as reported from electron spin resonance measurements [2]. Here, a rare trapping event triggers a defect precursor (Si dimer) to form an active defect (E' center). In this new configuration, the time constants for trapping and detrapping events are drastically reduced.

This is reminiscent of another intensively studied reliability issue — namely, random telegraph noise (RTS), where charge trapping is also assumed to proceed via LRME. Thorough investigations have shown classical calculations to be oversimplified and especially stressed the strong impact of quantization in the inversion layer on trapping dynamics [3, 4]. Hence, we extend the model by including quantum confinement and the impact of bound states on the trapping behavior.

II. MODELING

In our model the defect precursor is a neutral Si–Si dimer configuration (state 1, cf. Fig. 1) which features an electron level far below the silicon valence band edge. This defect can capture a hole which causes the rupture of the dimer bond and results in structural relaxation. While one of the silicon atoms takes the positive charge and relaxes backwards

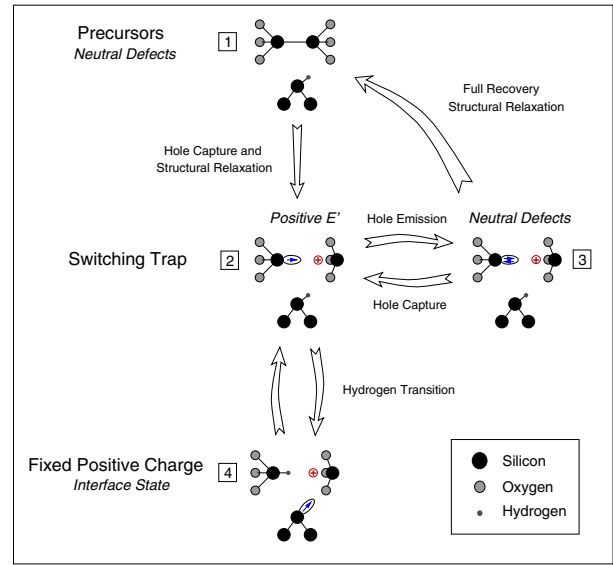


Fig. 1. Schematic representation of switching traps as suggested in [2]: State 1 depicts the precursor state, the Si–Si bond. The transition from state 1 to state 2 proceeds via hole capture followed by the subsequent break-up of the Si–Si bond and a structural relaxation to the E' center configuration. The positive charge can either be locked in the defect via a H reaction to state 4 or it can be annihilated by electron capture (state 3). For the latter case, two electrons reside at the Si dangling bond and the way back to state 1 is enabled by overcoming a thermal barrier.

to establish a weak bond to a nearby oxygen atom, the other silicon atom carries a dangling bond (state 2). The dangling bond (E' center) with an energy level close to the silicon band gap is capable of trapping or detrapping charge carriers from the silicon substrate. This repeated exchange of charge carriers with the substrate is reflected in switching back and forth between state 2 and 3. In its neutral charge state, it also may return back to its initial configuration by surmounting a thermal barrier. Alternatively, the defect in state 2 may undergo a reaction with the hydrogen attached to the final complex.

On the basis of this picture, trapping dynamics are described by the following set of rate equations

$$\partial_t f_1 = -f_1 k_{12} + f_3 k_{31}, \quad (1)$$

$$\partial_t f_2 = +f_1 k_{12} - f_2 k_{23} + f_3 k_{32} - f_2 k_{24} + f_3 k_{42}, \quad (2)$$

$$\partial_t f_3 = +f_2 k_{23} - f_3 k_{32} - f_3 k_{31}, \quad (3)$$

$$\partial_t f_4 = +f_2 k_{24} - f_4 k_{42} \quad (4)$$

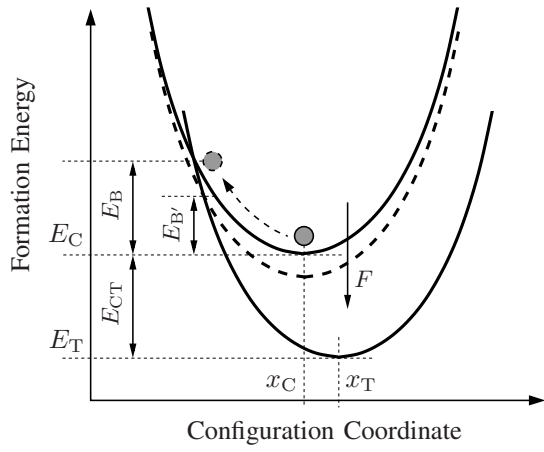


Fig. 2. Schematic representation of the LRME process: E_C and E_T are referred to as the formation energies of the entire system when the electron is situated either in the conduction band or in the trap. The corresponding configuration coordinates for their equilibrium positions are denoted as x_C and x_T . The parabola indicate the energy increase due to thermally induced distortions of the defect. According to MPFAT theory, the parabola is moved downwards upon application of an electric field F and thus the intersection point ($E_{B'}$) is considerably lowered. Mind that the overall barrier for a transition in the forward direction is E_B while it consists of $E_{CT} + E_B$ for the reverse direction, using the shorthand $E_{ij} = E_i - E_j$.

with the constraint

$$1 = f_1 + f_2 + f_3 + f_4, \quad (5)$$

where f_i corresponds to the occupation of the state i in Fig. 1 and k_{ij} denotes the transition rates from state f_i to state f_j . Hole trapping into the precursor state (k_{12}) is assumed to follow LRME theory [5]. The tunneling dynamics of the E' center is reflected in the rates k_{23} and k_{32} and is also described by LRME theory. The rate k_{31} accounts for the possibility of the neutralized E' center to collapse to its dimer configuration via thermal activation over a large barrier.

While the conventional concept of elastic tunneling rests upon the assumption that the initial electron level must coincide with the final defect level, LRME theory [5,6] solely accounts for the total energy conservation of the entire system including both the substrate and the dielectric. As illustrated in Fig. 2, the defect acquires just as much thermal energy in form of lattice distortions so that the total energy before and after the tunneling transition is preserved. This is the case when the system is found at the intersection point of the solid curves in Fig. 2. For a tunneling transition of the electron from the substrate conduction band into the trap, the system has to surmount an energy barrier E_B . For the reverse direction, the energy barrier is increased by E_{CT} .

The transition rates k_{12} , k_{23} and k_{32} read:

$$k_{f,n} = n \sigma_n v_n^{\text{th}} e^{-\beta E_B} \Theta(E_{TC}, e^{-\beta E_{TC}}, 1), \quad (6)$$

$$k_{r,n} = n \sigma_n v_n^{\text{th}} e^{-\beta E_B} \Theta(E_{TC}, e^{-\beta E_{FC}}, e^{-\beta E_{FT}}), \quad (7)$$

using the auxiliary function

$$\Theta(E_{\text{switch}}, a, b) = \begin{cases} a & E_{\text{switch}} \geq 0 \\ b & E_{\text{switch}} < 0 \end{cases} \quad (8)$$

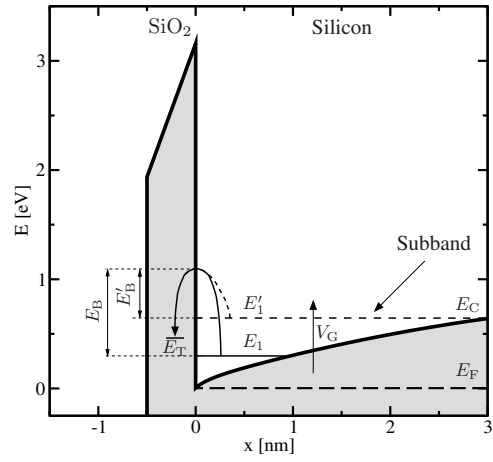


Fig. 3. Schematic of the LRME trapping process regarding quantization in the inversion layer. According to classical considerations, the electrons involved in LRME tunneling come from the conduction band edge E_C as computed by a Poisson solver for instance. In the quantum mechanical analogy, the lowest subband carries most of conduction band electrons and thus serves as the source of electrons for the LRME process. However, this subband is shifted upwards from E_1 to E_1' when the gate voltage V_G is raised. As a consequence, the energy barrier E_B for the LRME transition is decreased to E_B' . The position of the trap level as well as the LRME barrier have exponential dependences and thus are quite sensitive to changes in these quantities.

and the abbreviation $\beta = 1/k_B T$. E_C and E_T is the formation energy when the electron is in the substrate conduction band or in the trap state, respectively. E_F refers to the substrate Fermi level at the interface according to the assumption that the traps located close to the interface are controlled by the substrate Fermi level. n and v^{th} denote the electron concentration and their thermal velocity, respectively. The capture cross section σ_n includes the tunneling probability, modeled by a simple WKB approximation $\sigma_{n,0} \exp(-x/x_{n,0})$. Analogous rate equations hold for hole trapping [1]. According to multiphonon field-assisted tunneling (MPFAT) theory [7], a field-enhancement factor of the form $e^{(F/F_c)^2}$ is introduced in the above equations, where F stands for the electric field within the dielectric and F_c denotes a reference field.

The remaining transitions are thermally activated as follows:

$$k_{31} = \nu_1 e^{-\beta E_{B,3}}, \quad (9)$$

$$k_{24} = \nu_2 e^{-\beta(E_{B,f} - \gamma F)}, \text{ and } k_{42} = \nu_2 e^{-\beta(E_{B,r} + \gamma F)}, \quad (10)$$

where the corresponding attempt frequencies and barriers are referred to as ν_i and $E_{B,i}$. The term γF represents the rise or the lowering of the barrier due to the applied electric field.

So far, only classical calculations of the band edge energy diagram have been performed to obtain the interface quantities, in particular the position of the band edges (E_C , E_V), the Fermi level (E_F), and the electric field (F) within the dielectric. These quantities enter the expressions of the rates and will alter them significantly due to their exponential dependences. Equations (6) are valid for a three-dimensional electron gas [4], however, this assumption breaks down for an inversion layer of MOS structures. In the one-dimensional triangular potential well at the interface, bound states build up

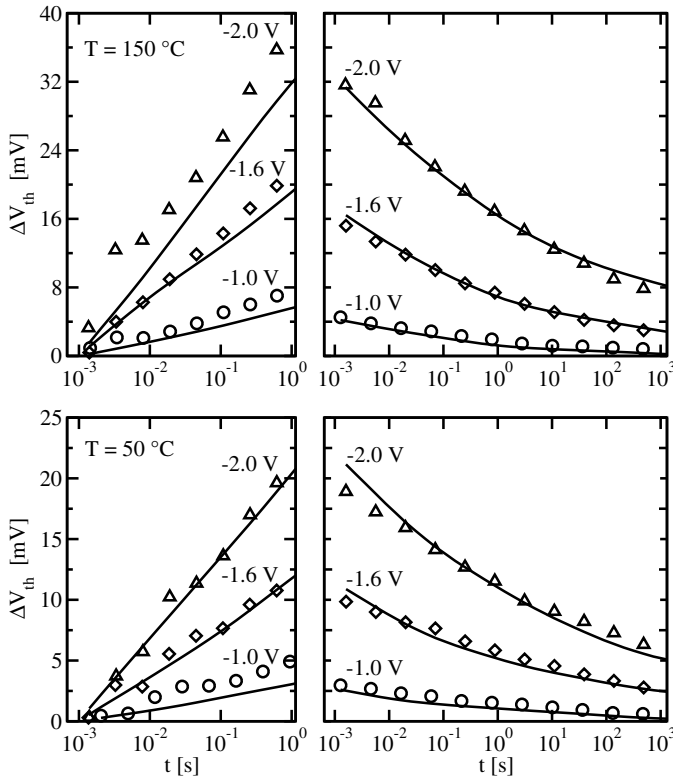


Fig. 4. Temporal evolution of the trapped charges neglecting quantization effects. A pMOSFET with a gate thickness of 1.75 nm is subjected to two different temperatures (50 °C upper panels, 150 °C lower panels) and three different gate voltages for 1 s (−1.0, −1.6, −2.0 V) during the first phase termed trapping/stress (left hand side). After the gate voltage is removed, the second phase called detrapping/relaxation phase (right hand side) sets in. Symbols mark measurement data while solid lines belong to simulation data. Note that the temperature and field dependence is well reproduced simultaneously for relaxation phase. The slight tendency of the simulations to underestimate the measurements at high stress temperatures during the stress phase may be traced back to the mobility degradation of the drain current during the MSM measurements [8].

and form subbands which correspond to the new initial or final energy levels for MPFAT transitions. For instance, electrons are energetically located in the bound states, which are situated above the conduction band edge and correspond to the new source of electrons in the respective rate expressions. With an increase in the applied gate bias, those bound states shift away from the conduction band which gives rise to different bias dependences (see Fig. 3).

III. SIMULATION AND RESULTS

We have performed numerical simulations on a pMOSFET structure with a 1.75 nm thick silicon oxynitride layer and an n-polygate. Due to the amorphous nature of silicon dioxide, a broad range of the electron energy levels and barriers must be expected. Defect levels and barriers associated with MPFAT trapping or detrapping processes are assumed to be homogeneously distributed while thermal barriers are taken to follow a Fermi-Derivative (Gaussian-like) distribution [9]. A large number of representative traps have been generated where each of them has an individual set of random energy levels and barriers. For the classical or quantum mechanical

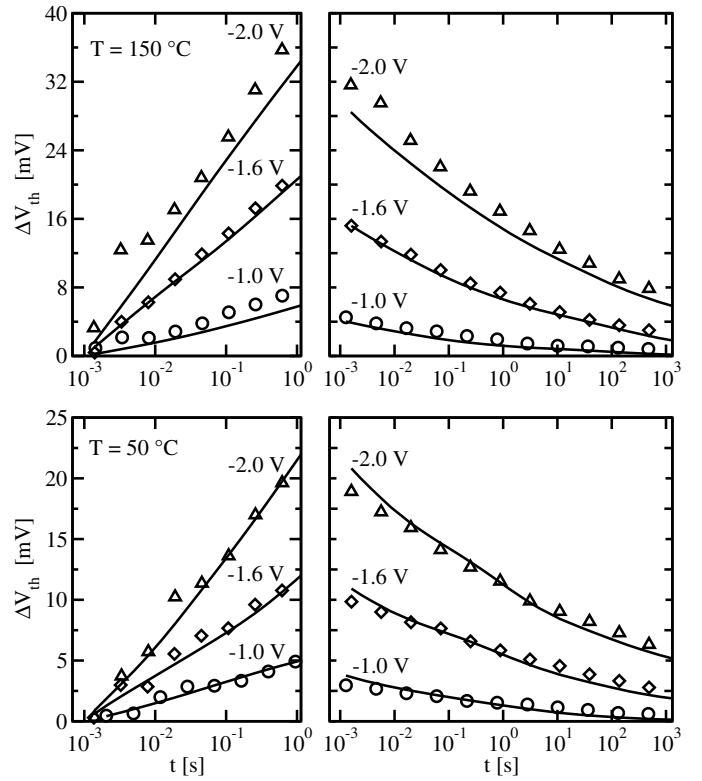


Fig. 5. The same as in Fig. 4 but for the quantum mechanical simulation. Again, very good agreement between recorded data and simulation data has been achieved.

calculation of the bandstructure, a self-consistent Schrödinger Poisson solver [10] has been applied that delivers the carrier concentrations, the band edge energies, the subbands, the Fermi level, and the electric field across the dielectric for the evaluation of the trapping rates. For each representative trap, the respective set of rate equations is solved numerically. The resulting threshold voltage shift is obtained by averaging over the occupations of state 2 and state 4.

For comparison, we first applied the classical variant of the trapping model to experimental data shown in Fig. 4. One can clearly recognize that good agreement with experimental data is achieved for two temperatures and three different gate voltages. The parameter values assumed in the simulations are listed on Table I. Note that the model has to satisfy the following three criteria to explain measurement data: First, the evolution of trapped charges follows a logarithmic time behavior for the early part of the stress phase as well as for the early part of the relaxation phase. Second, the slopes of the stress and the relaxation phase exhibit a certain ratio (~ 2.5 in the lin-log plots), which goes hand in hand with the fact that detrapping phase spans over a much larger time range compared with the preceding trapping phase. Third, the experimental data exhibits an approximately quadratic temperature and field dependence [1].

The trapping model including quantization effects has been applied to the same set of experimental data. For a proper comparison to the classical variant of the trapping model, only

TABLE I

VALUES USED FOR THE CLASSICAL (LEFT COLUMN) AND THE SUBBAND (RIGHT COLUMN) MODEL. ENERGY DISTRIBUTIONS ASSOCIATED WITH LRME TRANSITIONS ARE ASSUMED TO BE HOMOGENEOUS. NOTE THAT THE TRAP ENERGY LEVELS ARE REFERENCED TO THE SUBSTRATE VALENCE BAND. FOR SIMPLICITY, THE INITIAL ($V_I = 0.0$ eV) AND THE FINAL (V_F) VALUE OF THE TRANSITION $2 \leftrightarrow 4$ ARE TAKEN TO BE SINGLE-VALUED, WHILE THE ENERGY BARRIERS V_B ARE DISTRIBUTED FOLLOWING A FERMI-DERIVATIVE. ONLY A FRACTION ($C = 0.12$) OF ALL TRAPS IS ALLOWED TO UNDERGO THIS TRANSITION. NOTE THAT THE ATTEMPT FREQUENCY ν_2 SHOWS AN TEMPERATURE DEPENDENCE.

k_{12}		
σ_n	$1.08 \times 10^{-15} \text{ cm}^2$	$1.08 \times 10^{-15} \text{ cm}^2$
σ_p	$1.24 \times 10^{-14} \text{ cm}^2$	$1.24 \times 10^{-14} \text{ cm}^2$
$E_{T,\min}$	-1.14 eV	-1.64 eV
$E_{T,\max}$	-0.31 eV	-0.6 eV
$E_{B,\min}$	0.01 eV	0.03 eV
$E_{B,\max}$	1.15 eV	1.04 eV
F_c	2.83 MV/cm ²	3.66 MV/cm ²
k_{23} and k_{32}		
σ_n	$1.08 \times 10^{-15} \text{ cm}^2$	$1.08 \times 10^{-15} \text{ cm}^2$
σ_p	$1.24 \times 10^{-14} \text{ cm}^2$	$1.24 \times 10^{-14} \text{ cm}^2$
$E_{T,\min}$	0.01 eV	-0.35 eV
$E_{T,\max}$	0.3 eV	0.63 eV
F_c	none	none
k_{31}		
$\nu_{2,50C}$	$5.11 \times 10^{15} \text{ 1/s}$	$5.11 \times 10^{15} \text{ 1/s}$
$\nu_{2,150C}$	$3.54 \times 10^{10} \text{ 1/s}$	$3.5 \times 10^{10} \text{ 1/s}$
$V_{B,m}$	1.46 eV	1.46 eV
$V_{B,s}$	0.44 eV	0.44 eV
V_F	0.0 eV	0.0 eV
γ	0.74 nm	0.74 nm
k_{24} and k_{42}		
ν_1	10^{13} 1/s	10^{13} 1/s
$E_{B,\min}$	0.01 eV	0.01 eV
$E_{B,\max}$	1.15 eV	1.15 eV

the LRME parameters have been optimized while other all other parameters were held fixed. As shown in Fig. 5, the simulation shows very good agreement with experimental data reproducing the field- as well as the temperature dependence. Mind that the uppermost trap level E_T has been shifted downwards (Table I) in energy by about the same energy as the separation of the trap level from the conduction band edge (ranging between 169 and 277 meV). Since the changes in the occupancy in the states 3 and 4, the major fraction of injected holes into the dielectric will end up in states 2. Thus the field- and temperature dependence can be ascribed to the increase of the rates from states 1 into state 2 as demonstrated in Fig. 6. Due to the shift of the first subband and the field enhancement the factor rates increase with higher gate voltages, while higher temperatures raise the probabilities to overcome the LRME barriers.

IV. CONCLUSION

We have refined a recently published charge trapping model accounting for quantization effects. The consideration of subbands should affect the trapping dynamics as well as the extent of the field-acceleration. However, the required qualitative

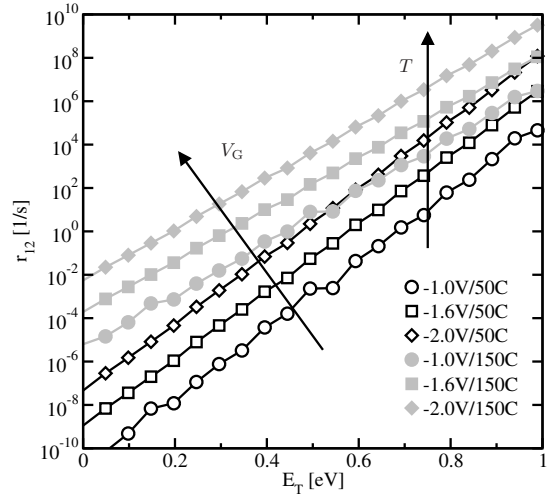


Fig. 6. Rates from state 1 into state 2 as a function of the trap levels E_T in state 1 (referred to the lowest trap). Due to the fact that nearly all injected charges accumulate in state 2, the injection rates are determined by the dependence of k_{12} on temperature and the electric field.

behavior, such as field- and temperature-acceleration along with the experimentally observed time scales covered by trapping and detrapping phase, are preserved. Nevertheless, a shift in the extracted model parameters, regarding the energy trap levels and the LRME barriers, has been observed.

REFERENCES

- [1] T. Grasser, B. Kaczer, W. Goes, T. Aichinger, P. Hehenberger, and M. Nelhiebl, "A Two-Stage Model for Negative Bias Temperature Instability," in *Proc. Intl.Rel.Phys.Symp.*, 2009, pp. 33–44.
- [2] A. Lelis and T. Oldham, "Time Dependence of Switching Oxide Traps," *IEEE Trans.Nucl.Science*, vol. 41, no. 6, pp. 1835–1843, Dec 1994.
- [3] R. Siergiej, M. White, and N. Saks, "Theory and Measurement of Quantization Effects on Si – SiO₂ Interface Trap Modeling," *Solid-State Electron.*, vol. 35, no. 6, pp. 834–854, 1992.
- [4] N. Zanolla, D. Siprak, P. Baumgartner, E. Sangiorgi, and C. Fiegna, "Measurement and Simulation of Gate Voltage Dependence of RTS Emission and Capture Time Constants in MOSFETs," in *Ultimate Integration of Silicon*, 2008, pp. 137–140.
- [5] M. Kirton and M. Uren, "Noise in Solid-State Microstructures: A New Perspective on Individual Defects, Interface States and Low-Frequency (1/f) Noise," *Adv.Phys.*, vol. 38, no. 4, pp. 367–486, 1989.
- [6] S. Makram-Ebeid and M. Lannoo, "Quantum Model for Phonon-Assisted Tunnel Ionization of Deep Levels in a Semiconductor," *Physical Review B*, vol. 25, no. 10, pp. 6406–6424, 1982.
- [7] S. Ganichev, W. Prettl, and I. Yassievich, "Deep Impurity-Center Ionization by Far-Infrared Radiation," *Phys.Solid State*, vol. 39, no. 1, pp. 1703–1726, 1997.
- [8] B. Kaczer, T. Grasser, P. Roussel, J. Martin-Martinez, R. O'Connor, B. O'Sullivan, and G. Groeseneken, "Ubiquitous Relaxation in BTI Stressing-New Evaluation and Insights," in *Proc. Intl.Rel.Phys.Symp.*, 2008, pp. 20–27.
- [9] A. Haggag, W. McMahon, K. Hess, K. Cheng, J. Lee, and J. Lyding, "High-Performance Chip Reliability from Short-Time-Tests," in *Proc. Intl.Rel.Phys.Symp.*, 2001, pp. 271–279.
- [10] M. Karner, A. Gehring, S. Holzer, M. Pourfath, M. Wagner, W. Goes, M. Vasicek, O. Baumgartner, C. Kernstock, K. Schnass, G. Zeiler, T. Grasser, H. Kosina, and S. Selberherr, "A Multi-Purpose Schrödinger-Poisson Solver for TCAD Applications," *J. Comput. Electronics*, vol. 6, pp. 179–182, 2007.