

# Characterization and Physical Modeling of the Temporal Evolution of Near-Interfacial States Resulting from NBTI/PBTI Stress in nMOS/pMOS Transistors

T. Grasser,\* B. Stampfer,\* M. Waltl,\* G. Rzepa,\* K. Rupp,\* F. Schanovsky,‡ G. Pobegen,†  
K. Puschkarsky,• H. Reisinger,• B. O'Sullivan,◦ and B. Kaczer◦

\*Institute for Microelectronics, TU Wien, Austria ‡ Global TCAD Solutions, Austria

† KAI, Villach, Austria • Infineon, Munich, Germany ◦ imec, Leuven, Belgium

**Abstract**—The last decade of BTI research has seen a frantic search for ultra-fast measurement methods to correctly understand the impact of fast as-grown traps which impact the initial phase (1ks) of the degradation and recovery. These methods focus mostly on determining the time-dependence of the threshold voltage shift. Other experimental methods able to resolve the energetic distribution of traps in the bandgap have recently not been as frequently employed as they introduce a large delay and also require switching the device into accumulation, which considerably accelerates recovery. Here we use detailed CV measurements to study NBTI/PBTI in SiO<sub>2</sub> nMOS/pMOS capacitors. We extract a unique defect band inside the SiO<sub>2</sub> insulator which can describe the build-up of near-interfacial states over time in all four combinations using our recently suggested gate-sided hydrogen release model. Our results suggest that depending on the transistor type and stress bias conditions, the generated slowly-recovering near-interfacial states are due to a combination of slower oxide traps and faster  $P_b$  centers.

## I. INTRODUCTION

Due to the rapid onset of recovery well below the microseconds regime and the fact that the accumulated degradation can be heavily perturbed by abrupt changes in the gate-bias, experimental challenges have been an important aspect of BTI research [1–6]. Conventionally used  $\Delta V_{th}(t)$  measurements sense the charge stored in the oxide and the interfacial layer for the surface potential close to the silicon valence band (in pMOS transistors) or close to the conduction band (in nMOS transistors). As such, these measurements are insensitive to the details of the charge distribution inside the bandgap and – as a consequence – a considerable amount of physically relevant information is missed. This includes for instance the flat-band voltage shift  $\Delta V_{FB}(t)$  and the details of the time evolution of the energetic profile of near-interfacial traps in the bandgap,  $D_{it}(E, t)$ , as for instance provided by CV measurements.

During the early days of bias temperature instability (BTI) research, CV measurements had been an integral part of the characterization procedure [7, 8]. It appears that together with the realization that BTI recovery – and in particular the accelerated recovery in accumulation – can heavily perturb measured threshold voltage shifts, the wealth of information contained in CV data [9] has recently received a limited amount of attention in favor of ultra-fast characterization methodologies [1, 10, 11]. However,  $\Delta V_{th}$  measurements only provide information on the threshold voltage and are virtually completely oblivious to the energetic profiles of the responsible defects. Compared to CP and DCIV measurements, CV measurements appear to provide the best energetic

resolution with a minimum amount of interference: While CP measurements require rapid changes between inversion and accumulation with incompletely understood consequences on the accumulated degradation [12], DCIV measurements [13, 14] are most sensitive to defects in the middle of the bandgap – which are the most efficient recombination centers – and the sensitivity of the method decreases exponentially with increasing distance from midgap.

Thus, in order to better understand the temporal change in the distributions of interfacial and oxide traps, we take a closer look at what can be learned from CV data and correlate the results with what has been learned during the last decade. This comparison is insofar of fundamental importance, as published models have focused on the evolution of  $\Delta V_{th}(t)$  and only qualitatively considered the evolution of the average of  $D_{it}(t) = \int D_{it}(E, t) \Delta E$ , while ignoring its energetic distribution. A word of caution is required here to clearly define the termini *interface* and *oxide states* as used in this work:

- **Experimental:** CV measurements are sensitive to all defects fast enough to respond to the AC frequency of the test signal,  $f_{AC}$ . These defects produce a true capacitive addition to the oxide capacitance,  $C_{it}$ . Also, CV measurements are sensitive to defects/charges in the oxide and at the interface,  $\Delta V$ , which result in a stretch-out of the CV curve. Experimentally, however, it appears impossible to clearly separate  $\Delta V$  and  $C_{it}$ . For instance, as will be elaborated below, the temporal change in the capacitance defined as  $\Delta C(t) = C(t) - C(0)$  will always contain a *mixture* of both,  $\Delta V$  and  $C_{it}$ .
- **Physical:** From a physical perspective, both interface defects (like  $P_b$  centers [15]) and defects deeper in the oxide (such as hydroxyl  $E'$  centers [16–18]) can contribute to both  $\Delta V$  and  $C_{it}$ , depending on the AC and the sweep frequency. Again, it is very difficult to clearly separate these defects using electrical measurements only. As will be shown, the conventional assumption that  $C_{it}$  in SiO<sub>2</sub>/Si devices – such as those used in the present study – is solely due to  $P_b$  centers is inconsistent with the presented data and the known electrical responses of  $P_b$  centers [19]. While  $P_b$  centers are definitely created during BTI stress [20], it has been repeatedly suggested that they are neither the only nor the dominant defects responsible for  $C_{it}$  and that a hydrogen-containing oxide defect is likely responsible [16, 21–25]. These arguments are consistent with our previous work [26–28] on the gate-sided hydrogen release mechanism (GSHR) [29].

- **Modeling:** In our gate-sided hydrogen release (GSHR) model, we will model the temporal evolution of interface defects,  $D_I$ , as well as the temporal evolution of oxide defects,  $D_O$ . We deliberately refer to these components with the unusual names  $D_I$  and  $D_O$  rather than using the conventional terms  $D_{it}$  and  $D_{ot}$ , since their experimental determination is very difficult and a multitude of definitions has been used. Compared to the experiment, the theoretical assignment is clear and both can contribute to  $\Delta V$  and  $C_{it}$ , depending on the experimental conditions.

In the following we will put forward experimental and theoretical evidence supporting the hydrogen release mechanism. One of the strengths of the GSHR model is that it provides a natural and fully consistent explanation both for the creation of oxide and interfacial states, which contribute to both  $C_{it}$  as well as  $\Delta V$ . For NBTI these defects are created near the channel by the release of neutralized protons from the gate side. For PBTI/pMOS, protons are released at the channel-side and also lead to the creation interface states as well as of defects at the gate side, which, however, are too far away from the channel to contribute to  $C_{it}$  and only impact  $\Delta V$ . Finally, due to their energetic alignment, no protons are neutralized during PBTI/nMOS, which leads to negligible degradation. It is particularly this asymmetry that leads us to argue that studying *all four combinations*, N/PBTI in n/pMOS, is required to reveal the true physics of BTI degradation [30, 31], even though from an industrial perspective only the most relevant combination NBTI/pMOS is typically considered.

## II. EXPERIMENTAL

Recent results have established that BTI essentially follows the same dynamics in production-quality SiO<sub>2</sub>, SiON, and high-K devices [32–34], all of which seem to be dominated by the Si/SiO<sub>2</sub> interfacial region. Since our goal is the understanding of the fundamentals of BTI rather than any particular latest technology, we use large-area (50 μm × 50 μm) pMOS and nMOS capacitors on ⟨100⟩ Si with p- and n-poly gates, respectively. To avoid excessive leakage currents, which would interfere with our CV measurements, we chose relatively thick SiO<sub>2</sub> capacitors with  $t_{ox} = 5$  nm. To assure comparability between the different measurement frequencies, the frequency was varied from 1 kHz to 1 MHz in the *inner loop* to avoid recovery induced by bias changes [35]. In the outer loop, the voltage was swept from −2 V to 2 V. Otherwise, if for instance the low-frequency CV curves had been measured first followed by the higher frequencies, the highest frequency CV curve would have seen a considerably amount of recovery which could be mistaken for a frequency dependence.

## III. CV CONSIDERATIONS

Before we begin to analyze our CV data, we investigate the expected response using theoretical models. Regrettably, the extraction of interface state profiles,  $D_{it}(E)$ , from CV data is notoriously difficult and various methods have been suggested [36], none of which is free of artifacts: for example, a high-frequency reference curve measured for instance at 1 MHz still contains the response of interface states while

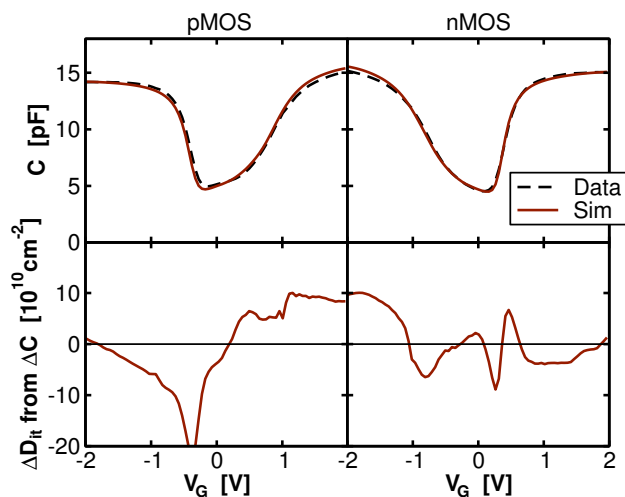


Fig. 1. **Top:** Comparison of measured (dashed) and simulated CV (solid line) for pMOS (left) and nMOS (right). **Bottom:** Despite the use of a sophisticated TCAD model, the difference  $\Delta C$  between measurement and simulation is on the order of  $10^{11} \text{ cm}^{-2}$  when converted to an effective interface density of states, which is on the same order of magnitude as the degradation built up in our experiments.

it is extremely challenging to create precise theoretical CV reference curves for devices with non-trivial doping profiles, non-ideal interfaces and oxides. This problem is illustrated in Fig. 1, in which a TCAD simulator was calibrated to measured CV curves of the MOSCAPs used in this study. Data were recorded in the temperature range 125 – 175°C and the 150°C fits together with the difference between data and simulation,  $\Delta C$ , converted to an effective density of interface states ( $\Delta D_{it} = \Delta C/q_0A$ , with  $A$  being the device area and  $q_0$  the elementary charge), are shown. Despite the use of sophisticated TCAD models (Fermi-Dirac statistics, quantum correction models [37, 38], non-uniform doping profiles, poly-depletion, temperature-dependent band-edges, effective masses, density-of-states and thermal velocities), the difference  $\Delta C$  is on the order of  $10^{11} \text{ cm}^{-2}$  when converted to an effective interface density of states. While the fits of the CV curves are apparently very good, this effective  $D_{it}$  is of a similar magnitude as the recorded drifts in this work and would thus severely compromise the accuracy of the extracted  $D_{it}$  profiles.

Due to the reasons stated above, we do not attempt to extract  $D_{it}(t)$  but rather restrict our analysis to the change of  $D_{it}$  over time,  $\Delta D_{it}(t) = D_{it}(t) - D_{it}(0)$ . In the simplest case,  $\Delta D_{it}(t)$  is obtained from  $\Delta C = C(t) - C(0)$ , as has been done previously [9]. However, it is important to realize that the shift  $\Delta C$  is not necessarily due to the true capacitive contribution caused by interface states but can contain a large spurious component caused by the bias-dependent stretch-out  $\Delta V(V_G)$ .

This problem is elaborated using the calibrated TCAD model for the nMOS and pMOS capacitors. We start with Fig. 2, which shows  $\Delta C$  resulting from a rigid shift of the CV curve by  $\Delta V = -30$  mV, caused e.g. by fixed positive charges. Thus, in this simplest case  $\Delta C(V_G) = C(V_G - 30 \text{ mV}) - C(V_G)$ . This value of  $\Delta V$  was chosen to be similar to the  $\Delta V$  observed in our experiments. As can be seen, a spurious  $D_{it} \approx 2 \times 10^{11} \text{ cm}^{-2}$  is obtained, which has of course nothing to do

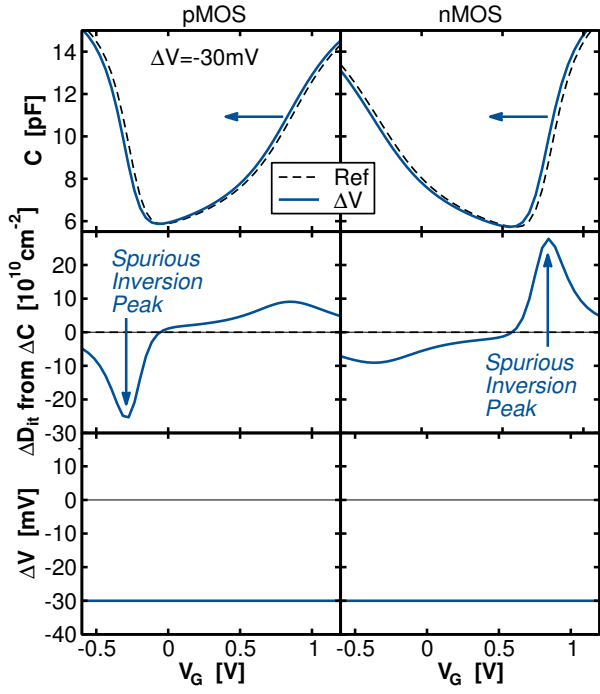


Fig. 2. **Top:** Simulated change in  $C(V_G)$  resulting from a rigid shift by  $\Delta V = -30$  mV. **Middle:** The  $D_{it}$  extracted from  $\Delta C(V_G) = C(V_G + \Delta V) - C(V_G)$  is completely spurious and a direct consequence of the shift  $\Delta V$ . Particularly when  $dC/dV_G$  is large, e.g. in inversion, very large peaks are obtained. **Bottom:** The trivial  $\Delta V$ .

with the creation of interface states. On the other hand, Fig. 3 shows the resulting contribution of  $P_b$  centers to  $D_{it}$  assuming a density of  $15 \times 10^{10} \text{ cm}^{-2}$ , also in the range of what will be measured later. For this value of  $D_{it}$ , a maximum  $\Delta V = -35$  mV is obtained in our 5 nm oxides, provided all defects can follow the CV sweep rate  $S$ . The parameters of the characteristic double humps of the  $P_b$  centers were taken to match the data of [19], with the normally distributed  $+0$  transition levels at 0.235/0.125 eV (mean/variance) and the  $0/-$  transition at 0.8/0.1 eV. These values were obtained on 2.9 nm thick  $\text{SiO}_2$  grown on  $\langle 100 \rangle$  wafers, which should be quite similar to our devices.

In this simulation study it is possible to separate the impact of the true capacitive contribution  $C_{it}$  and the spurious impact of the stretch-out  $\Delta V$ . For example, for a reasonably slow sweep rate  $S$  (around 1 V/s) and a very high AC frequency  $f_{AC}$  outside feasible experimental ranges, only  $\Delta V$  will be relevant and  $C_{it}$  will be zero. As can be seen in the middle panel of Fig. 3, the shape of the characteristic humps is hardly affected by the stretch-out  $\Delta V$ . In a number of publications it has been observed that the number of created fast states,  $D_{it}$ , appear to be in a 1:1 relation with fixed oxide charges [7, 8]. A similar conclusion was drawn by Huard *et al.* [39], who observed a strong shift in  $V_{th}$  while no shift in  $V_{FB}$  was seen. Such a scenario is theoretically investigated in Fig. 4. However, the impact of the additional  $Q_{ox}$  on  $\Delta C$  appears to be small as the energetic position of the created  $D_{it}$  is rather close to the band-edges.

Finally, typical CV/GV signals produced by oxide defects modeled via a two-state non-radiative multiphonon (NMP)

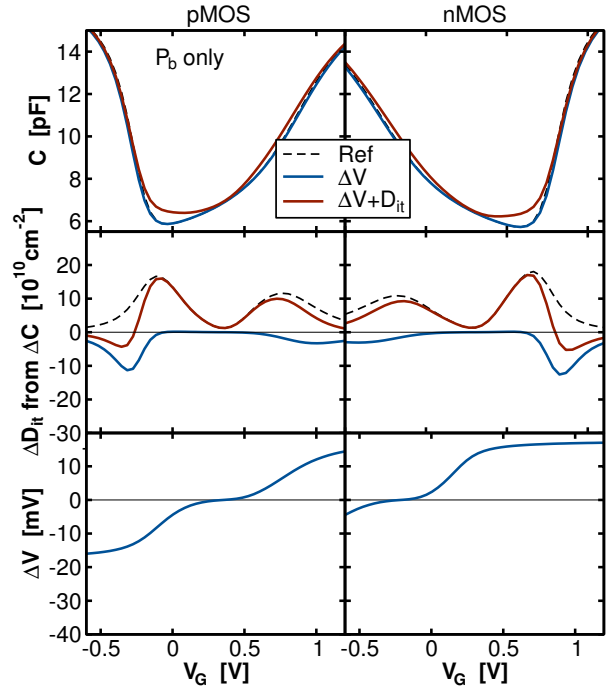


Fig. 3. **Top:** Simulated change in  $C$  for a  $P_b$  center density of about  $15 \times 10^{10} \text{ cm}^{-2}$  at  $f_{AC} = 1$  MHz and  $S = 1$  V/s. In addition to the stretch-out caused by  $\Delta V$  (solid blue line), the capacitance shows a true increase by  $\Delta C_{it}$  (solid red) compared to the reference curve (dashed). **Middle:** The  $D_{it}$  extracted from  $\Delta C$  (solid red) is only little affected by the change in  $D_{it}$  caused solely by the stretch-out  $\Delta V$  (solid blue), e.g. at very high  $f_{AC}$ . The dashed black line is the true  $\Delta C_{it}$  put into the simulator. **Bottom:**  $P_b$  centers are positively charged below midgap and negatively charged above, the integral of which determines  $\Delta V$ .

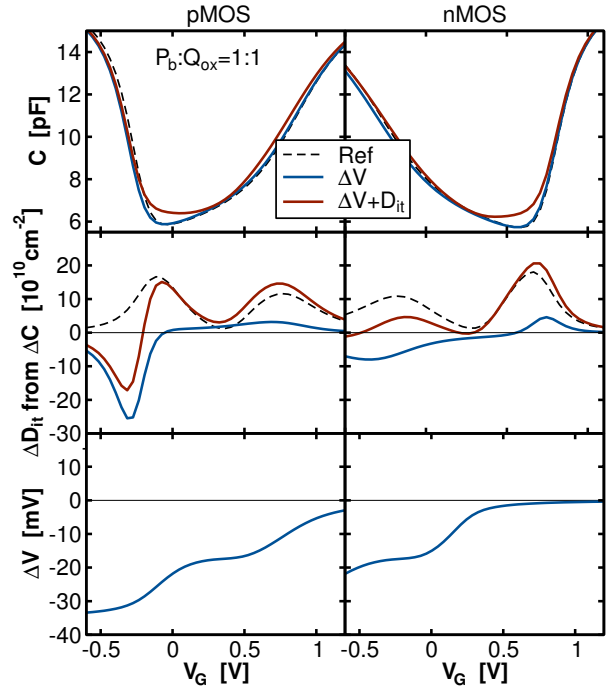


Fig. 4. **Top:** Like Fig. 3 but assuming that  $P_b$  centers and fixed positive charges are created in a 1:1 ratio. **Middle:** Due to the larger amount of charges below midgap, the  $D_{it}$  extracted from  $\Delta C$  is more strongly affected by the stretch-out  $\Delta V$  in the lower half of the bandgap. **Bottom:** For each  $P_b$  center, a positive charge is added, resulting in zero net charge for large positive  $V_G$ , thereby rigidly shifting the  $\Delta V$  curve of Fig. 3.

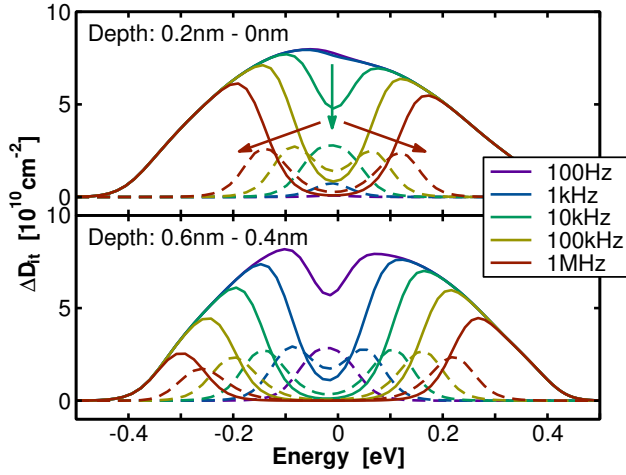


Fig. 5. Theoretical  $\Delta D_{it}$  from a two-state NMP defect normally distributed in energy around midgap with  $\sigma = 0.2\text{eV}$  and over a certain depth into the oxide of a pMOS: right at the interface (top) and  $0.5\text{nm}$  into the oxide (bottom). Solid lines are CV and dashed lines GV. Note the similarity to the symmetric NBTI/nMOS case of Fig. 8.

model are shown in Fig. 5 for a broad normally-distributed defect band directly at and slightly displaced from the channel, which already shows some resemblance with the present data shown in Section IV. Notably, defects around midgap have the largest time constants and can thus only follow lower CV frequencies. In particular, this slow response of midgap states explains the characteristic “dip” in the data at typically used higher frequencies, which makes this broad distribution split into two peaks, quite reminiscent of the well-known peaks of  $P_b$  centers shown in Figs. 3 and 4.

#### IV. RESULTS

Based on these theoretical considerations we can investigate the impact of NBTI and PBTI stress on the CV characteristics of both our p- and nMOSCAPs. As can be seen in Fig. 6, BTI stress has a marked impact on the CV curves in three out of the four cases considered:

- PBTI on pMOS capacitors leads mostly to the creation of positive charges visible as similar  $\Delta V_{th}$  and  $\Delta V_{FB}$  shifts plus a small contribution in  $D_{it}$ .
- Quite to the contrary, PBTI on nMOS capacitors does neither lead to an appreciable amount of positive charges nor a change in  $D_{it}$  as both  $\Delta V_{th}$  and  $\Delta V_{FB}$  are very small.
- NBTI, on the other hand, leads to comparable shifts in  $\Delta V_{th}$  and  $\Delta V_{FB}$  for both n- and pMOS capacitors when one considers that they are given by  $\Delta V_v = \Delta V(E_v)$  and  $\Delta V_c = \Delta V(E_c)$ , which are invariant to the transistor type and correspond to  $\Delta V_{th}$  and  $\Delta V_{FB}$  in a pMOS and vice versa for an nMOS.

In order to approximately separate  $\Delta C$  caused by the stretch-out  $\Delta V$  and the “true” contribution due to fast states, we follow a pragmatic approach and compare a CV measured after stress,  $C(V_G)$ , to a reference curve taken before stress,  $C_0(V_G + \Delta V)$ . Irrespective of the precautions taken, extraction of  $\Delta D_{it}$  and  $\Delta V$  will always be prone to errors and should be used with

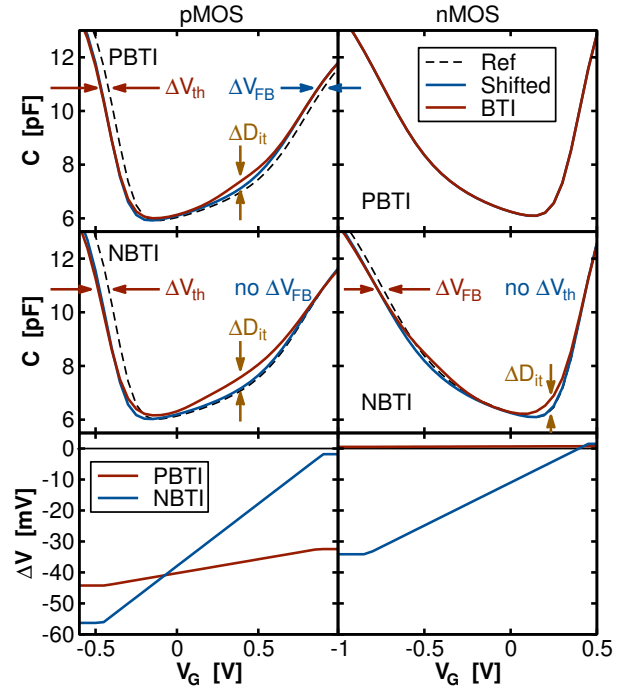


Fig. 6. The reference CV (dashed line, measured at  $100\text{kHz}$ ) corrected by the stretch-out (solid blue), approximated by  $\Delta V_{th}$  and  $\Delta V_{FB}$  and linearly interpolated in-between. The difference is converted to  $\Delta D_{it}$ . NBTI ( $-5.5\text{V}/125^\circ\text{C}/7.5\text{ks}$ , middle row) results in similar changes in both n/pMOS while PBTI ( $+5.5\text{V}/125^\circ\text{C}/7.5\text{ks}$ , top row) leads to positive charging (pMOS) but has no impact on the nMOS. The approximate  $\Delta V$  is shown in the bottom row.

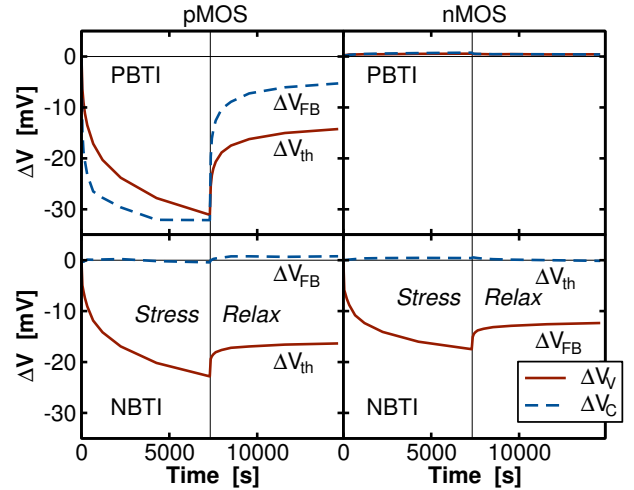


Fig. 7. Temporal evolution of  $\Delta V_c$  and  $\Delta V_v$  for NBTI/PBTI (stress at  $\pm 4.5\text{V}/125^\circ\text{C}/7.5\text{ks}$  and recovery at  $0\text{V}/125^\circ\text{C}/7.5\text{ks}$ ). The corresponding conventional notation  $\Delta V_{th}$  and  $\Delta V_{FB}$  is also given.

care. In the following, we will merely use this separation for visualizing the changes in CV data, while for matching the data with the model the full  $\Delta C$  curves are taken. This is done since particularly for technologically relevant drifts the changes would otherwise be difficult to visualize, see Fig. 6, which already uses relatively high stress voltages ( $\pm 5.5\text{V}$ ). In order to visualize  $\Delta D_{it}$ , we will use a pragmatic ansatz for  $\Delta V$  by first approximately extracting  $\Delta V_v$  and  $\Delta V_c$ . Then,

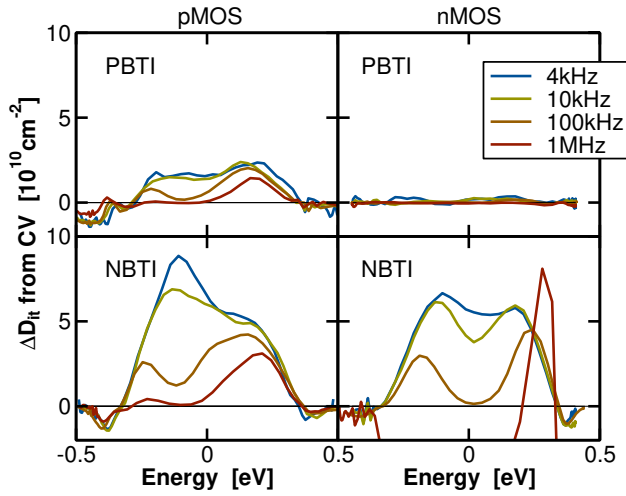


Fig. 8. Extracted  $\Delta D_{it}$  profiles for the four cases using the simple correction scheme explained in Fig. 6. NBTI results in similar  $\Delta D_{it}$  for nMOS and pMOS, while PBTI in pMOS produces mostly positive charge. Conversely, PBTI on nMOS has little impact on  $\Delta D_{it}$ .

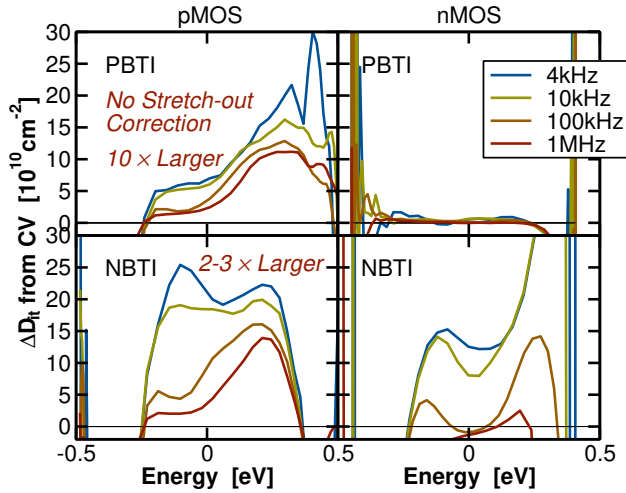


Fig. 9. Spurious  $\Delta D_{it}$  resulting from simple  $\Delta C$  extraction without correction for the stretch-out. While for NBTI  $\Delta D_{it}$  can be 2-3 $\times$ larger, a 10 $\times$ larger  $\Delta D_{it}$  is obtained for PBTI/pMOS.

a corrected reference curve is constructed by mapping the original CV curve onto these points and stretching it linearly in-between, see Fig. 6 (bottom). This corresponds to assuming a constant defect density between  $E_c$  and  $E_v$  (as a function of  $V_G$  and thus only approximately constant as a function of the surface potential) and any deviation in the real  $D_{it}$  will result in a spurious  $\Delta D_{it}$ .

Extracted  $\Delta D_{it}$  profiles for the considered four cases are shown in Fig. 8. NBTI creates the largest  $\Delta D_{it}$  in both nMOS and pMOS, while PBTI creates positive charges in pMOS but hardly affects the nMOS. The impact of the stretch-out is shown in Fig. 9. If neglected, a simple  $\Delta C$  will show spurious peaks (typically negative in the lower half of the bandgap and positive in the upper-half), which can be easily confused with  $\Delta D_{it}$ . While the NBTI cases result in a 2-3 times overestimation, the massive amount of positive charge

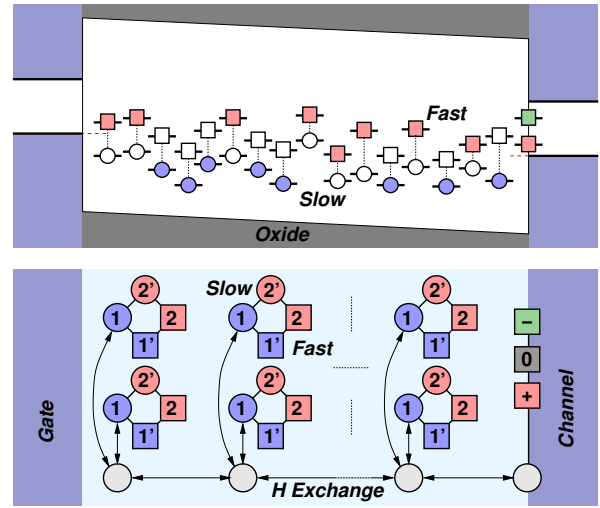


Fig. 10. A schematic view of the H-release model using 4-state NMP defects (H-complexes). In the primary configuration ( $\circ$ ) the trap level is lower than in the distorted secondary configuration ( $\square$ ). From state 1, the H can be released, leaving an inactive precursor site behind. The released H can either bind to an empty precursor to create a new active defect or depassivate a  $P_b$  center at the interface, which can then be either positive, neutral, or negative, depending on the Fermi level position.

for PBTI/pMOS gives nearly a 10 times large spurious  $\Delta D_{it}$  if the stretch-out is not considered.

Typically it is *implicitly* assumed that  $\Delta D_{it}$  is due to the creation of  $P_b$  centers. In contrast,  $\Delta D_{it}$  peaks have been occasionally observed to move between the lower and upper half of the bandgap [40], an observation sometimes explained by suggesting the involvement of different defect types evolving with different kinetics. When comparing the measured CV peaks to what is expected from  $P_b$  centers, it is quite obvious that  $P_b$  centers alone cannot explain the data: As shown in Figs. 3 and 4,  $P_b$  centers form peaks in the lower and upper half of the bandgap which grow in unison, the energetic location of which show little frequency dependence [41]. It has been previously suggested that – although ESR-active  $P_b$  centers are also created – the *dominant* defect is ESR-inactive and possibly hydrogen related [42].

## V. THEORETICAL MODELING

We extend our previously suggested gate-sided hydrogen-release model [26–28] for NBTI to describe the present data. In the first formulation of this mechanism we only modeled  $\Delta V_{th}$  and the precise location of the defects inside the bandgap was not considered. For this purpose we used a simple two-state NMP model for the hydrogen-related defects, most likely hydroxyl  $E'$  centers, which were allowed to release H from their neutral state. For modeling CV data, the more complicated nature of the defects has to be taken into account: TDDS studies have clearly shown that oxide defects have at least four active states and their transitions are described by two defect levels [17, 43]: the slow transitions between states 1 and 2' typically have an energy level close to  $E_v(\text{Si})$ , while the faster transitions between states 2 and 1' (the “switching trap level”) are inside the Si bandgap and dominate the CV response.

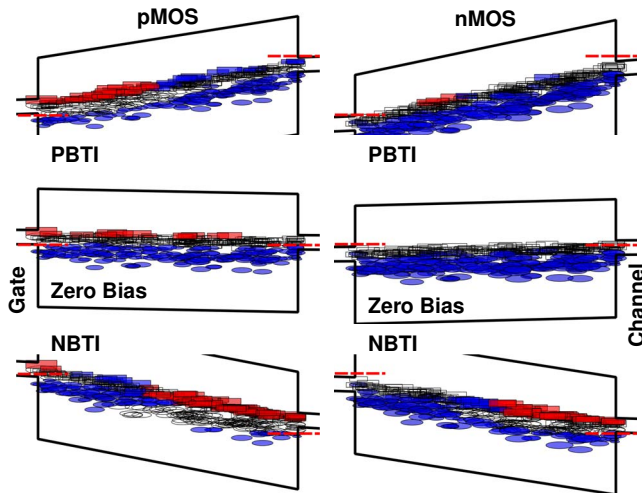


Fig. 11. Creation of charge and defects for the four cases. In the middle row the initial zero bias case is shown. Neutralized defects (blue) can release hydrogen, which preferentially moves to regions where it can create a positive defect (red). The circles correspond to the primary configuration (states 1 and 2') while the squares represent the secondary configuration (states 2 and 1'). Empty symbols are precursor defect sites without a trapped H.

These four-state defects are now coupled by a hydrogen exchange mechanism via the neutral state 1, see Fig. 10. Application of negative/positive bias for the nMOS and pMOS results in charge transitions at those defect sites, see Fig. 11, as well as H release from neutralized sites. H then quickly migrates to newly available sites which support a positive – and thus preferred – defect site, see Fig. 12. In addition, the released H can easily depassivate SiH bonds to create  $P_b$  centers [42].

In a first attempt to extend the GSRH model from two-state to four-state defects, we expressed the problem as a complete state machine with hydrogen now hopping between the four defect states of each defect and the neutral interstitial position. However, this formulation results in a very large equation system and severe numerical difficulties related to the vastly different time scales (TS) implied by the individual processes:

- TS1 Hydrogen diffusion is extremely fast, leading to extremely fast H redistribution. For instance, using established parameters for H diffusion [44], at 125°C interstitial H would hop to a next neighbor separated by 5 Å in a nanosecond.
- TS2 Charge trapping and detrapping in the secondary configuration of the four-state defects ( $2 \leftrightarrow 1'$ ) occurs over a wide range of time scales. This begins with very fast transitions being able to contribute to  $D_{it}$  for defects very close to the interface (with time constants in the milliseconds to microseconds range) and ends at slower defects deeper in the oxide or those with larger relaxation energies (seconds).
- TS3 Activation of defects via a transition from the primary to the secondary configuration ( $1 \rightarrow 2$ ) is relatively slow, with time constants in the microseconds to kiloseconds regime.
- TS4 Finally, release of H from the defects proceeds over a

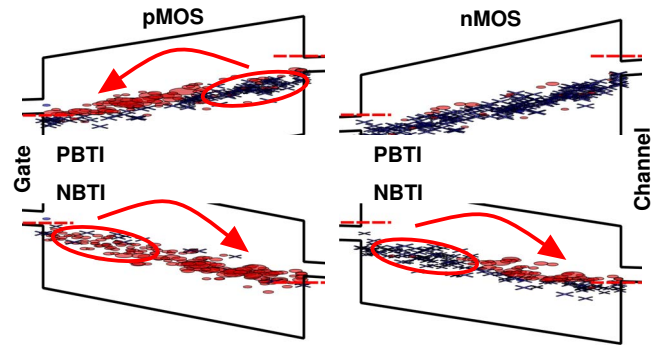


Fig. 12. Depending on the bias scenario and the transistor type, H relocates from different regions. For NBTI/pMOS, H comes predominantly directly from the gate and moves to a relatively wide region near the channel, explaining the asymmetric frequency dependence. For NBTI/nMOS, the H comes from a relatively broad region on the gate-sided half of the oxide and moves closer to the channel interface. For PBTI/pMOS mostly positively charged defects are created while for PBTI/nMOS no H relocates. The crosses mark the locations where H is lost while the red circles mark the newly activated defect sites relative to the initial equilibrium solution.

barrier in range of 1–1.5eV, leading to time constants well above the kiloseconds regime.

As a consequence of the various time scales, the resulting equation system was found to be extremely stiff and as such ill-conditioned and virtually unsolvable even with sophisticated quad-precision solvers such as PETSc [45]. In order to obtain a tractable model, the following simplifications were introduced:

- Hopping of interstitial H is so fast that no spatial distribution of H needs to be considered. As such, a simple H reservoir can be used which couples with the individual defects. This approximation is equivalent to assuming quasi-equilibrium at time scale TS1.
- While not perfectly accurate, the time scales TS2/TS3 and TS4 were separated by assuming the transitions  $0 \leftrightarrow 1$  to be the slowest.

Using the above two simplifications, a relatively simple model is obtained which is nearly trivial to solve (compared to the original problem), as it consists only of a large number of independent  $4 \times 4$  linear equation systems (for which a closed form solution exists), which feed into a single non-linear equation with one unknown, the amount of interstitial  $H^0$ : Based on a given density of oxide traps (or precursors, depending on the availability of H), oxide traps are activated. Their (virtual) occupancy is dynamically calculated using an implicit Euler algorithm as done previously for four-state oxide traps [46], leading to the occupancies in the states  $f_1, f_2', f_2$ , and  $f_1'$ .

Contrary to our previous version of the model, which considered the number of H atoms in each state, we now introduce the total number of trapped hydrogens at each “oxide site”,  $H_{T,i}$ . An “oxide site” could for instance be one of the N-member rings in the SiO<sub>2</sub> matrix but are here introduced for numerical convenience so that not every H trapping site needs to be considered individually. Each “oxide site” can trap  $H_{T,max}$  hydrogens by exchanging H with the  $H^0$  reservoir from state 1. Using the previously calculated occupancies for

the current time-step, the number of positively charged defects in state 2 can then be obtained as  $f_2 \times H_{T,i}$ .

The dynamics of each  $H_{T,i}$  are then given by the following ordinary differential equation

$$\frac{dH_{T,i}}{dt} = -k_{10,i}f_{1,i}H_{T,i} + k_{01,i}H^0(H_{T,\max} - H_{T,i}). \quad (1)$$

Note that each  $H_{T,i}$  at each time-step can be calculated independently of all the others. Using an implicit Euler scheme, the above can be easily solved for a time-step  $\Delta t$  to give

$$H_{T,i} = \frac{aH_{T,i}^{\text{old}} + k_{01,i}H^0H_{T,\max}}{a + k_{10,i}f_{1,i} + k_{01,i}H^0} \quad (2)$$

with  $a = 1/\Delta t$ . Since the total number of hydrogens in the system must stay constant ( $H_{\text{tot}}$ ) at every time-step, we obtain the following equation to express this hydrogen conservation

$$H^0 + \sum_i H_{T,i} = H_{\text{tot}} \quad (3)$$

which via substitution of eq. (2) results in

$$H^0 + \sum_i \frac{aH_{T,i}^{\text{old}} + k_{01,i}H^0H_{T,\max}}{a + k_{10,i}f_{1,i} + k_{01,i}H^0} = H_{\text{tot}}. \quad (4)$$

At every time-step eq. (4) is a simple non-linear equation with one unknown ( $H^0$ , the number of interstitial hydrogens) which was found to be very stable and converge quickly within a few iterations using a Newton algorithm. With  $H^0$  available, the individual  $H_{T,i}$  can be calculated using eq. (2). With  $H_{T,i}$  available, the contribution of each trapping site to the stretch-out  $\Delta V$  can be calculated by considering its positive charge in the states 2 and 2'. Thus, one obtains

$$\Delta V = \frac{q_0 t_{\text{ox}}}{A\epsilon} \sum_i H_{T,i} (f_2 + f_{2'}) \left(1 - \frac{x}{t_{\text{ox}}}\right), \quad (5)$$

with  $x$  being the distance of the trap from the interface and  $\epsilon$  the permittivity of  $\text{SiO}_2$ . Analogously, the individual contributions to the total capacitance as well as the conductance at a particular bias can be obtained as

$$\Delta C_{\text{it},i} = \frac{\Delta C_{\text{it},i}^0}{1 + \left(\frac{\omega}{k_i}\right)^2}, \quad (6)$$

$$\frac{\Delta G_{\text{it},i}}{\omega} = \Delta C_{\text{it},i} \frac{\omega}{k_i}, \quad (7)$$

with the auxiliary quantities

$$\begin{aligned} \Delta C_{\text{it},i}^0 &= q_0 H_{T,i} \frac{k'_{21',i} f_{2,i} - k'_{1'2,i} f_{1',i}}{k_i}, \\ k_i &= k_{1'2,i} + k_{21',i}, \\ k'_{1m,i} &= \frac{dk_{1m,i}}{dV_G}, \end{aligned}$$

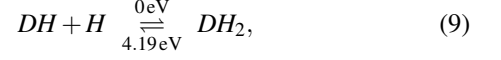
and the angular frequency  $\omega = 2\pi f$ . Hereby it was assumed that only the fast transitions in the secondary configuration ( $2 \leftrightarrow 1'$ ) contribute to the capacitance.

Before proceeding to the calculation of the interface states, we remark on an important approximation employed above: in order to keep the model tractable, it was assumed that a defect can either exist as a precursor,  $D_0$ , or as an active defect,  $DH$ .

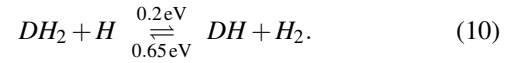
Activation of the defect requires the capture of a hydrogen via the reversible reaction



As outlined previously using DFT calculations [47], however, an active defect can capture another H and become inactive via

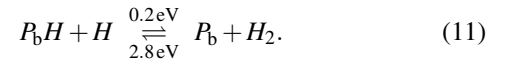


where the average barrier is given above/below the arrows for the case of the hydroxyl E' center. At a first glance this reaction implies that provided sufficient H is available, all precursors will be passivated and inactive,  $DH_2$ . However, when  $DH_2$  encounters a third hydrogen, the defect is reactivated via

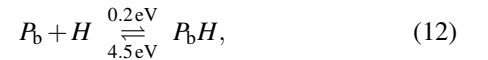


As a consequence, the actual concentration of active defects,  $DH$  will depend on the complicated interplay of  $H$ ,  $H_2$ ,  $D$ ,  $H_{T,\max}$ , and the solubility of  $H_2$  in the surrounding material layers. While the barriers for the various reactions have been estimated using DFT calculations in [47], the prefactors remain unknown, making the correct consideration of the above reactions challenging. To keep the model complexity within bounds, all  $H_2$  related reactions have therefore been neglected in the derivation of the model.

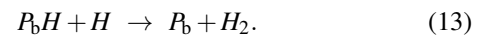
In addition to the rearrangement of H inside the oxide, which leads to the creation of oxide defects,  $D_0$ , the highly reactive H can also depassivate  $P_b$  centers: Once the H is released inside the oxide, the creation of interface states can be calculated via



Conversely, depassivated dangling bonds will also be easily passivated again via the released  $H$  via



making the whole process extremely complicated, in particular since both  $P_b$  and D compete for the released  $H$ . At the present stage of understanding, we keep the model tractable by only considering the forward reaction of eq. (11)



Furthermore, we assume that the density of  $P_b$  centers is relatively small and thus the competition for  $H$  is negligible. Then, the concentration of  $P_b$  centers can be calculated independently of  $D$ , resulting basically in the reaction-limited model proposed by Huard *et al.* [31] with a forward rate only.

Finally, the model is calibrated to the experimental data in Fig. 13. Shown are all four cases of NBTI/PBTI in pMOS and nMOS using the same universal defects bands. As discussed above, the model was calibrated to  $\Delta C$  to include both the contribution of the stretch-out as well as the creation of fast states. Excellent agreement with experimental data is obtained for the build-up of  $\Delta D_{\text{it}}$  near the interface, as well as the

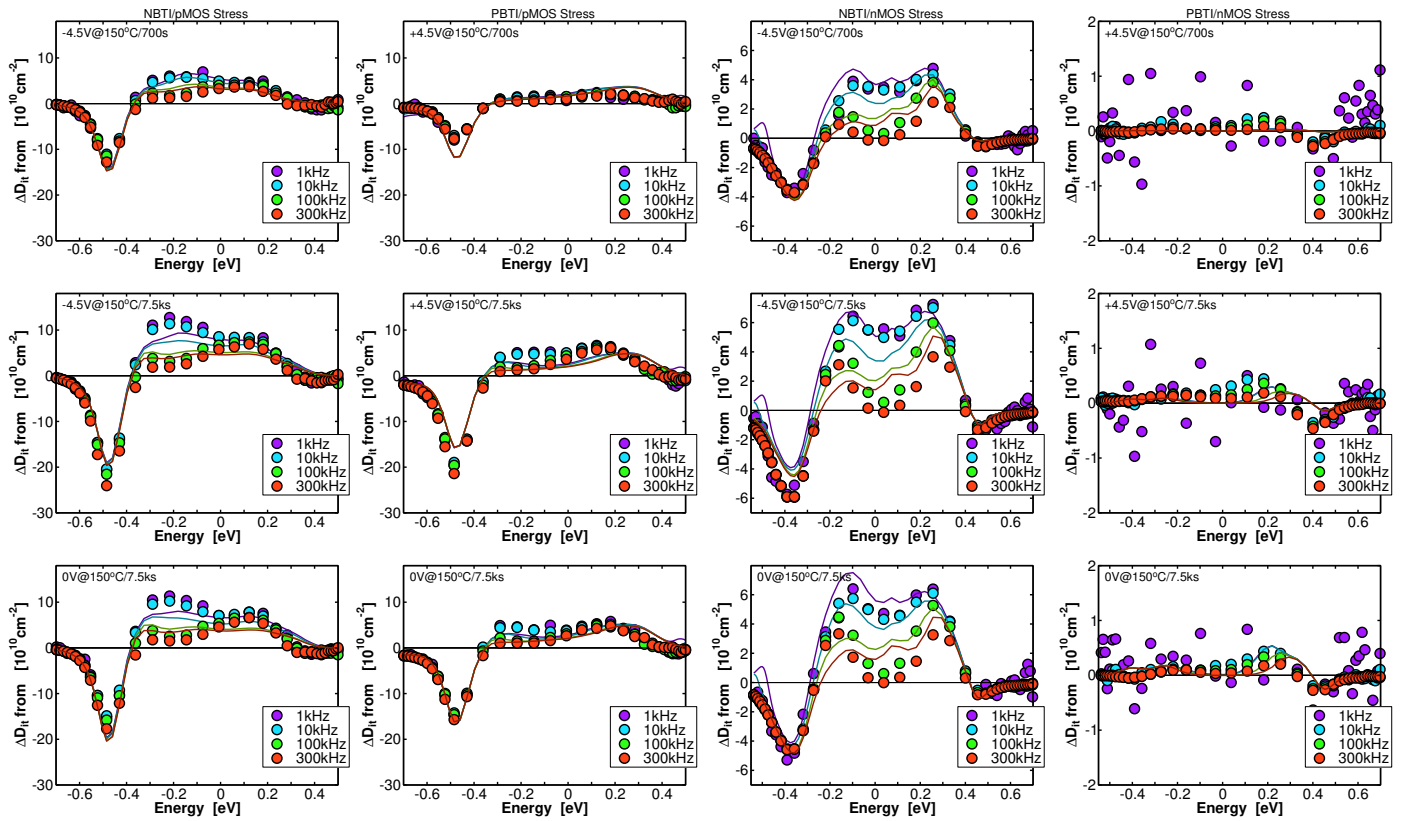


Fig. 13. Comparison of measured and simulated  $\Delta D_{it}$  profiles from  $\Delta C$  measurements for the four combinations of NBTI/PBTI in pMOS/nMOS during both two stress times (top, middle) and after a 7.5ks recovery (bottom). Note the zoomed y-axis scales for the two nMOS cases to better bring out the details. The gate-sided hydrogen-release model captures both the build-up of  $\Delta D_{it}$  over time as well as the energetic location and the asymmetric frequency dependence. The noise visible particularly for the NBTI/nMOS case is due to the stochastic solution method employed.

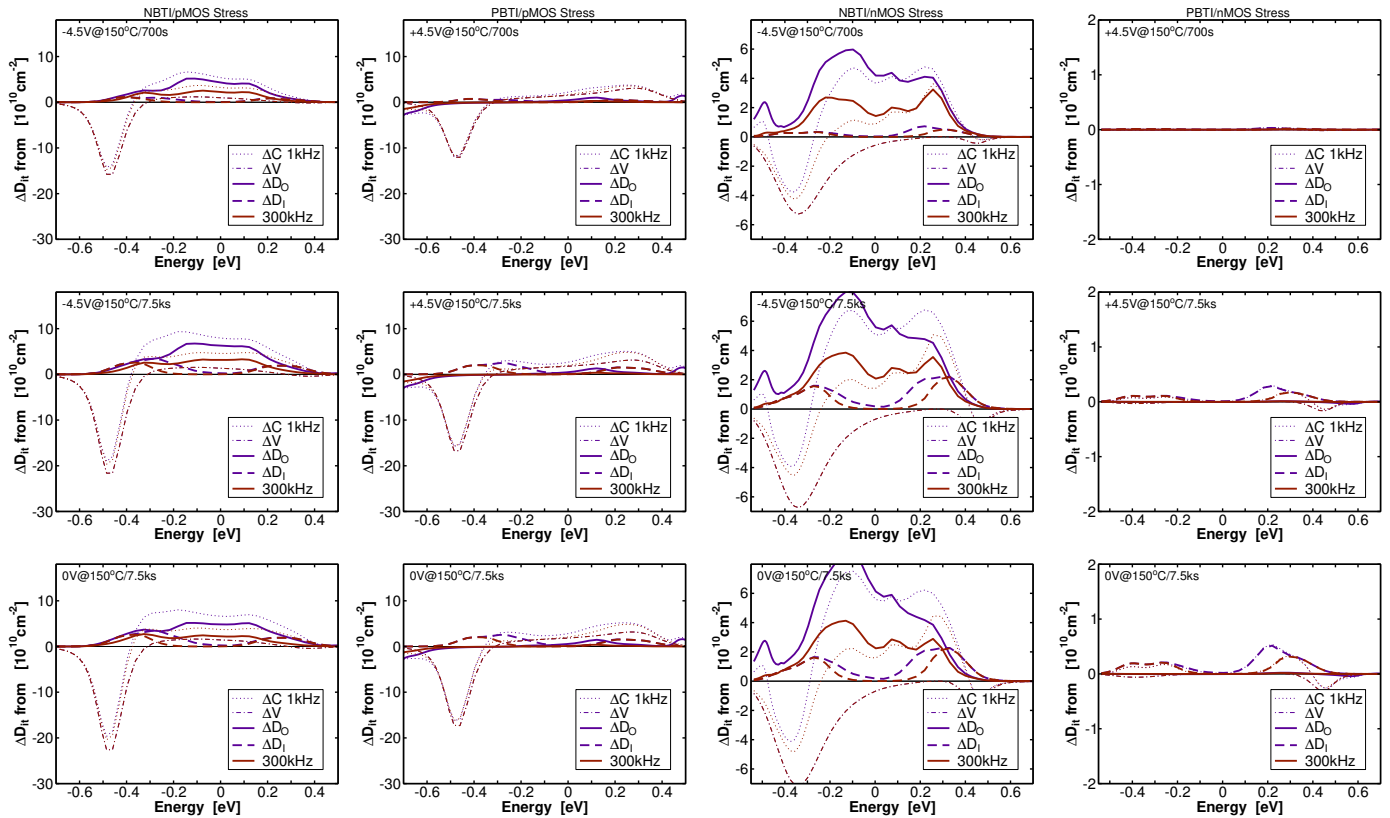


Fig. 14. The details of the simulation of Fig. 13 showing how the three components,  $\Delta V$ ,  $\Delta C_{it}$  and  $\Delta C_{ot}$  contribute to the overall observed  $\Delta C$  and thus  $\Delta D_{it}$ . Again note the zoomed y-axis scales for the two nMOS cases to better bring out the details.



peculiar frequency dependence: While NBTI/nMOS results in a narrow depth distribution close to the channel interface, which is symmetric in its frequency dependence in the lower and upper half of the bandgap, NBTI/pMOS produces a deeper distribution that results in an asymmetric frequency dependence.

As can be clearly seen by the large negative peaks in  $\Delta D_{it}$ ,  $\Delta C$  contains a significant *spurious*  $\Delta D_{it}$  contribution which is not due to fast states but merely due to the stretch-out  $\Delta V$ . This stretch-out is due to  $D_O$ , that is, defects which are too slow to contribute to  $\Delta D_{it}$  and thus primarily act as positive charge. The individual contributions are shown in Fig. 14. The contributions to the “real”  $D_{it}$  is strongly frequency dependent and  $\Delta C_{it}$  only dominant at higher frequencies above 100kHz. Only for the PBTI/pMOS case,  $D_{it}$  is mostly due to  $P_b$ -centers.

## VI. CONCLUSIONS

We have studied slow NBTI/PBTI degradation in nMOS/pMOS capacitors using detailed frequency-dependent CV measurements. Using our previously suggested gate-sided hydrogen release model we explain a wide range of features observed in the data. Consistent with previous results, our data confirm that while hints of the well-known energetic distribution of  $P_b$  centers are visible in our data,  $P_b$  centers can neither account for the majority of  $\Delta D_{it}$  nor the even larger stretch-out, particularly at lower CV frequencies. In addition, we argue that there is a large contribution of oxide defects that dominates the permanent degradation after BTI. We demonstrate for the first time that the unique defect band of these *spatially distributed hydrogen-related defects* can fully explain the degradation for all four stress combinations. It is also this spatial distribution which is responsible for the observed strong frequency dependence. Contrary to NBTI in both pMOS and nMOS, where hydrogen is released at the gate-side of the SiO<sub>2</sub>, during PBTI/pMOS the neutralization of positive defects at the channel side leads to the release of hydrogen there. Finally, for the PBTI/nMOS case, only a small number of hydrogen-related defects are neutralized and little hydrogen is released, leading to a very small overall degradation. In addition, the highly reactive hydrogen released during the neutralization of oxide defects can consistently explain the creation of  $P_b$  centers, which has so far remained a mystery.

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