

Simulation studies of nanostructured thermoelectric materials

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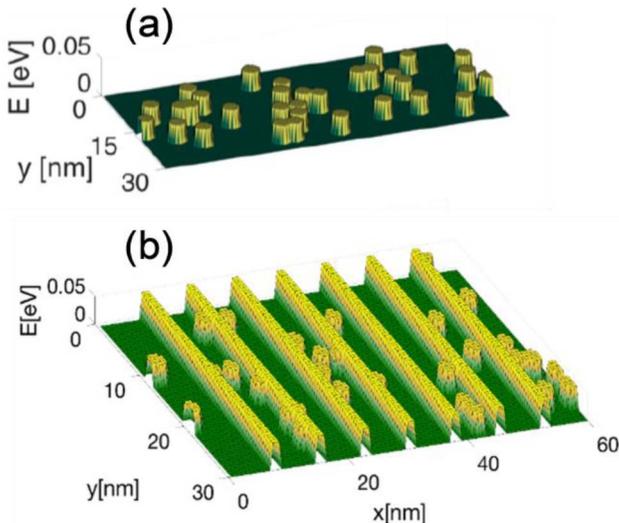


Fig. 1. (a) A schematic of a material geometry with embedded nanoinclusions of diameter $d = 3$ nm randomly placed in the material. (b) A hierarchically nanostructured material geometry, where nanoinclusions of 3 nm in diameter are embedded within a superlattice structure. Both the nanoinclusions and superlattice boundaries are treated as potential barriers for charge carriers with barrier height V_B .

Abstract— In this work we use modelling and simulation to investigate directions in achieving large thermoelectric figure of merit ZT in hierarchically nanostructured materials. To this end we explore both reduction of thermal conductivity and improvement of the power factor. We employ a series of models and computational techniques, from analytical models to fully quantum mechanical non-equilibrium Green’s function simulations and to large scale Monte Carlo simulations. We show that nanostructuring across different length scales, which can drastically reduce the thermal conductivity of thermoelectric materials, can also be designed to retain, or even improve the power factor.

I. INTRODUCTION

Nanostructured materials have shown great potential to achieve extremely low thermal conductivities, and thus, improve thermoelectric performance. Hierarchical nanostructuring, where phonons of different wavelengths scatter on features of different sizes have resulted in $ZT > 2$ [1]. However, less attention has been paid to the possibility of designing nanostructured materials for improving the thermoelectric power factor (PF). Recent experimental and theoretical works by us and others, however, have indicated that nanostructuring can indeed provide additional improvements from the power factor [2-8]. For effective

reduction in thermal conductivity and power factor improvements, advanced simulations, which can capture all essential transport physics and nanoscale geometry details, are needed.

In this work, we present our simulation efforts to address both electronic and phononic transport in nanostructured materials. With regards to electronic transport, we employ the fully quantum mechanical electron transport non-equilibrium Green’s function method (NEGF) for two-dimensional nanocomposites of arbitrary geometries. We present simulations for thermoelectric power factor in materials with embedded nanoinclusions (NIs) and voids, and hierarchical geometries where nanoinclusions are placed within nanograins as shown in **Fig. 1**. The NEGF formalism captures all important effects that relate to the different and non-uniform geometrical features extending from a few to hundreds of nanometers. With enough computational power, electron-phonon interactions are also included, which allows treatment from fully coherent to incoherent, and from fully ballistic to diffusive transport regimes. With regards to phonon transport, we present our newly developed phonon Monte Carlo simulator and our simulation work on highly disordered nanostructured materials. Finally, we discuss our efforts to include insight from atomistic techniques (e.g. Density Functional Theory and Molecular Dynamics) into the simulators, allowing a more accurate treatment of electrothermal transport at the nanoscale.

II. KEY RESULTS

We first consider the influence of nanoinclusions alone on the power factor and for this investigation we employ the diffusive (incoherent) scattering regime. The thermoelectric power factors versus barrier height V_B are shown in **Fig. 2** for four simulated geometries as shown in the insets of **Fig. 2**. The four simulated geometries are: i) a 2×4 array of NIs (green lines), ii) a 4×4 array (black lines), iii) a 6×4 array (blue lines), and iv) an 8×4 array (red lines). The Fermi level is placed at $E_F = 0.05$ eV (dashed-red line in **Fig. 2**), i.e. we consider degenerate high carrier concentration conditions. Increasing density decreases the electrical conductance, but increases the Seebeck coefficient. The result is that the power factor increases from the pristine channel value by $\sim 10\%$ until the barrier height of the NIs reaches $k_B T$ below the Fermi level, and for higher barrier heights it drops. This indicates that NIs provide some filtering of the low energy carriers, although its magnitude is lower compared to that of superlattices. The dashed lines extend the NI barrier height to infinity, resembling a void/porous structure. In that case the power factor saturates to a lower value compared to the

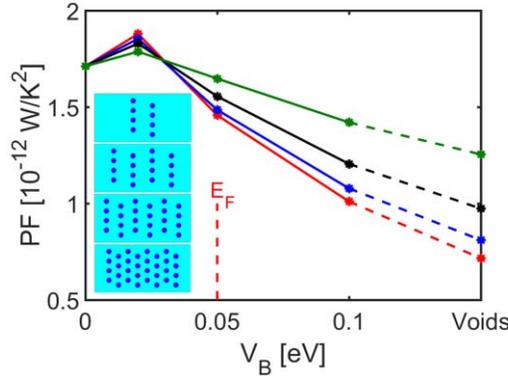


Fig. 2. The power factor in a material geometry with embedded nanoinclusions of increasing density as indicated in the insets. The position of the Fermi level is indicated by the dashed red line. The power factor is independent of the nanoinclusion density for low barrier heights V_B (below the Fermi level). The far right points indicate the power factor in structures with voids, i.e. $V_B = \text{inf.}$ (Results adapted from Refs. [8] and [9])

pristine structure. However, the important point to stress here is that the influence of the NI density is not significant on the power factor when their barrier heights are low compared to the Fermi level. Thus, high NI densities can be employed to maximize the reduction in thermal conductivity without degrading the power factor, and even resulting in a small positive effect.

Next, we perform simulations for the power factor in geometries that include superlattice-type barriers and NIs, as shown in Fig. 1b. For simplicity we only consider one superlattice period and examine two different NI densities for varying barrier height values. For this simulation we include electron-phonon scattering in the NEGF simulator, considering both acoustic and optical phonons. The power factor of this geometry as the NI barrier height increases is shown in Fig. 3. The superlattice barrier height is indicated by the dashed line. The Fermi level is placed at $E_F = 0.07$ eV. Again, as above, for very low NI barrier heights, below the superlattice barrier and E_F , the influence of the NIs on the power factor is small, especially for lower densities. Higher densities reduce the power factor at high barrier heights, however the overall reduction is of the order of $\sim 20\%$, which indicates that it might be worth to use high densities if the thermal conductivity is degraded by a larger amount. We are currently undertaking efforts to estimate the influence of NIs, voids and boundaries of various densities and configurations on the thermal conductivity. Although in this work we did not consider any specific material, we are also undertaking efforts using atomistic methods to include material specific parameters in the simulations.

III. CONCLUSIONS

In conclusion, we present a study of the thermoelectric power factor in nanostructured materials with nanoinclusions and superlattice-like barriers, geometries that are often employed to reduce the thermal conductivity of materials. We show that small barrier heights under degenerate conditions

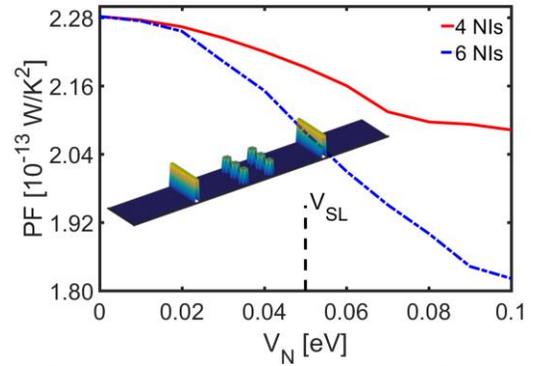


Fig. 3. The power factor in a material geometry as in Fig. 1b, with embedded nanoinclusions within a superlattice type structure (for simplicity only one superlattice period is simulated) vs the nanoinclusion barrier height V_N . The superlattice barrier height is indicated by the dashed line. The results for two different nanoinclusion densities are shown.

can have a very small positive effect on the power factor independent of nanoinclusion density, which could be a useful direction for obtaining large thermoelectric performance using nanostructures.

ACKNOWLEDGMENT

This work has received funding from the European Research Council (ERC) under the European Union's Horizon 2020 Research and Innovation Programme (Grant Agreement No. 678763).

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